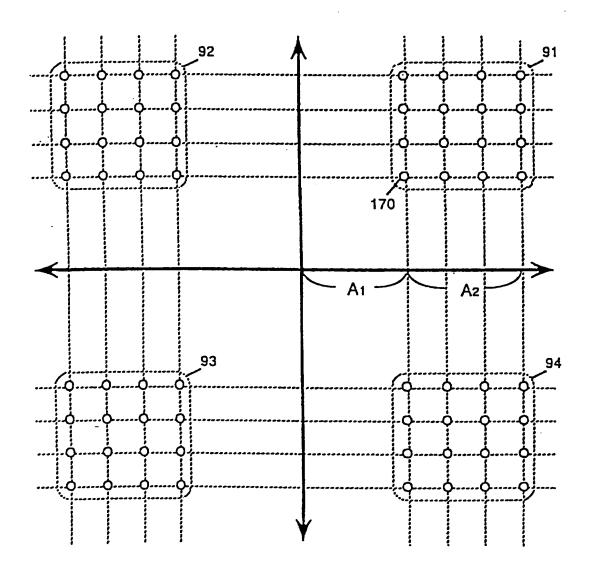


4 Fı

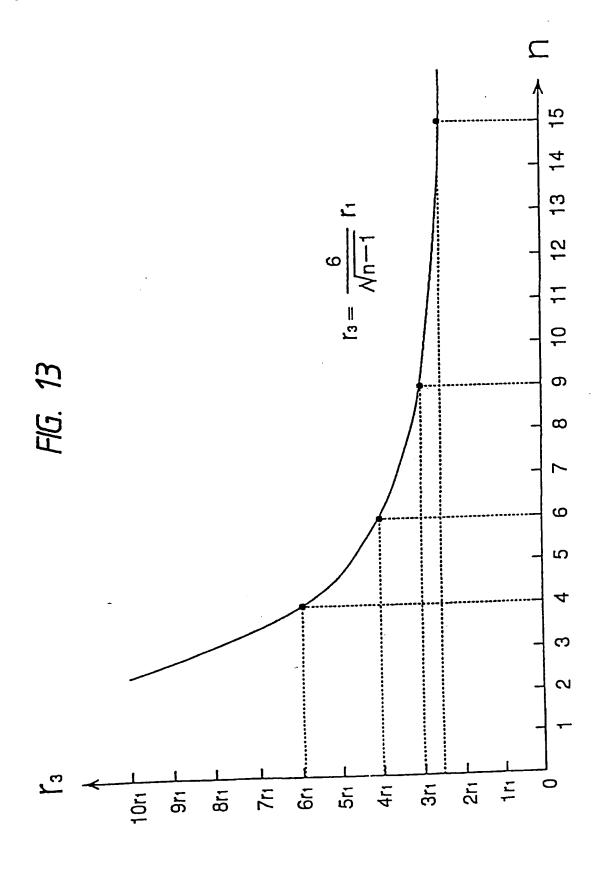
FIG. 12

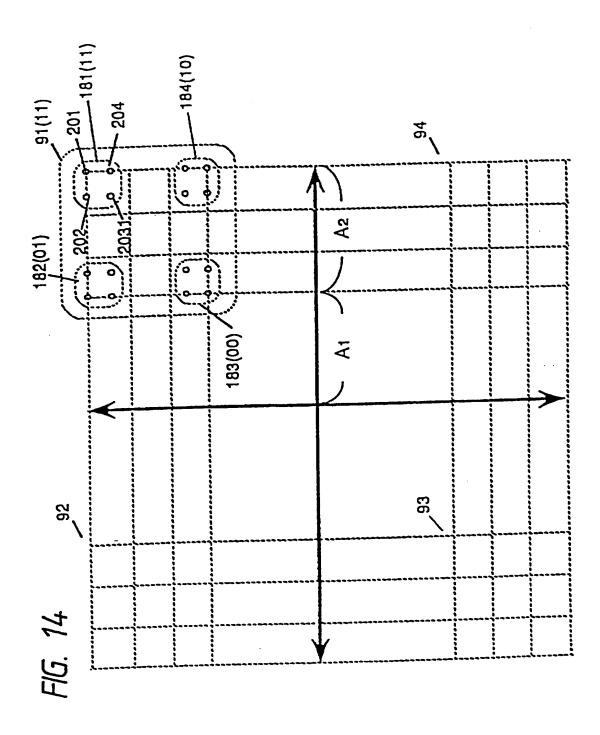


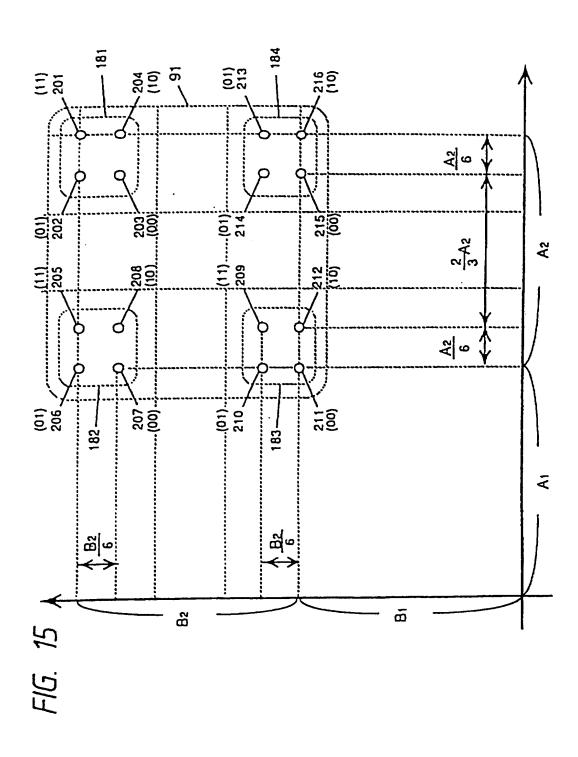
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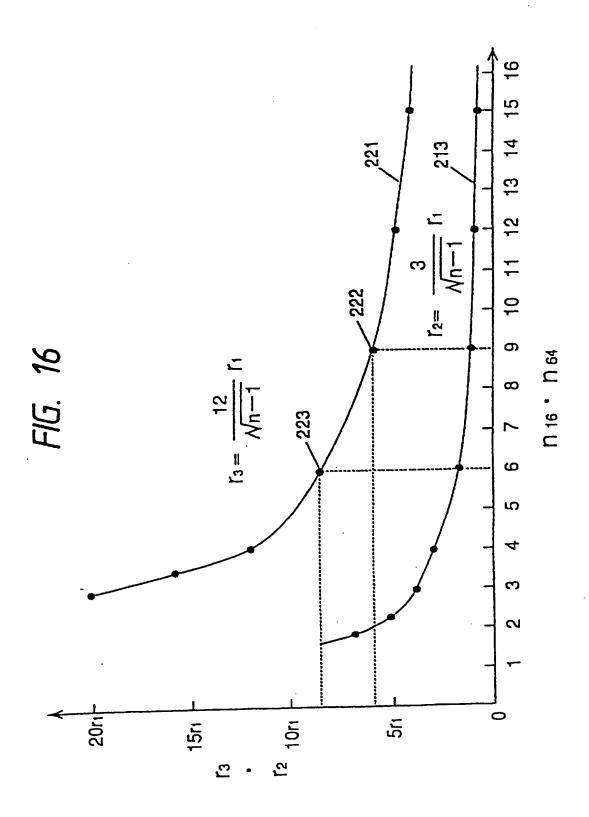
Sheet 13 of 174

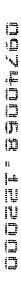


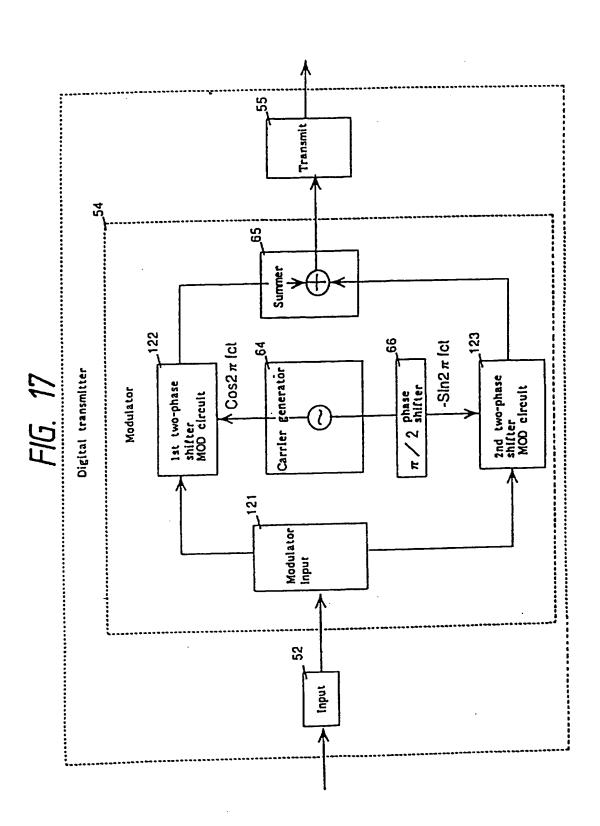


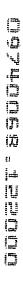


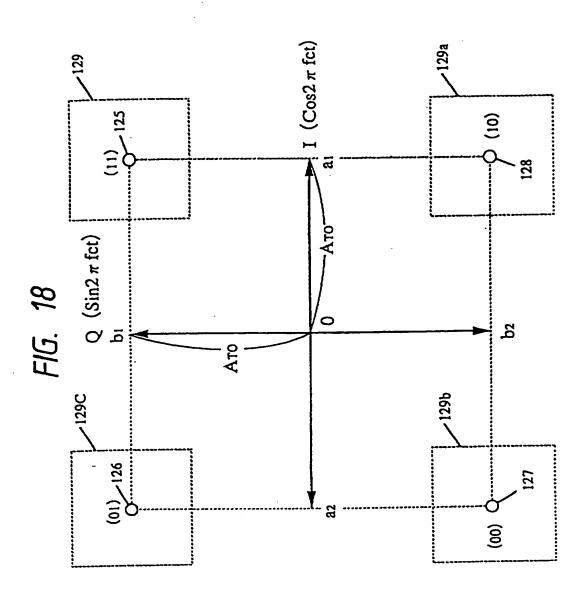












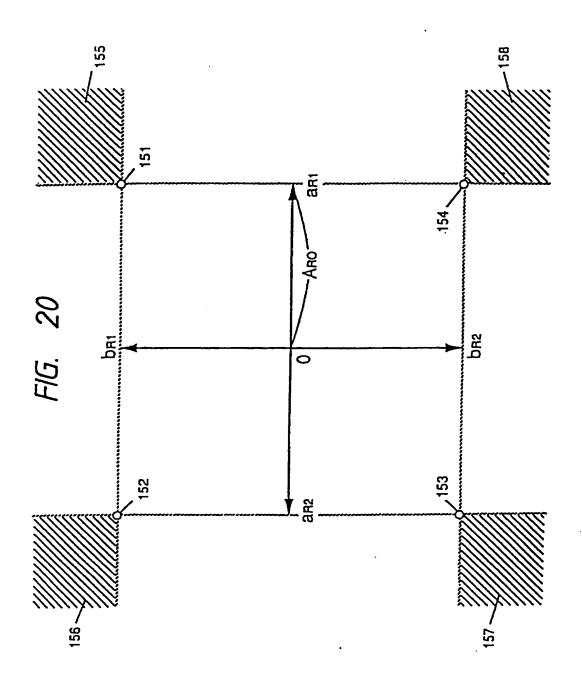
U.S. Patent 5,802,241 Sep. 1, 1998 Sheet 19 of 174 232 Ist data stream reproducer Timing extractor 2nd DISCR/ REPRO circuit 2nd phase detector 1st phase detector *FIG.* 19 1st Receiver Demodulator Carrier reproducer

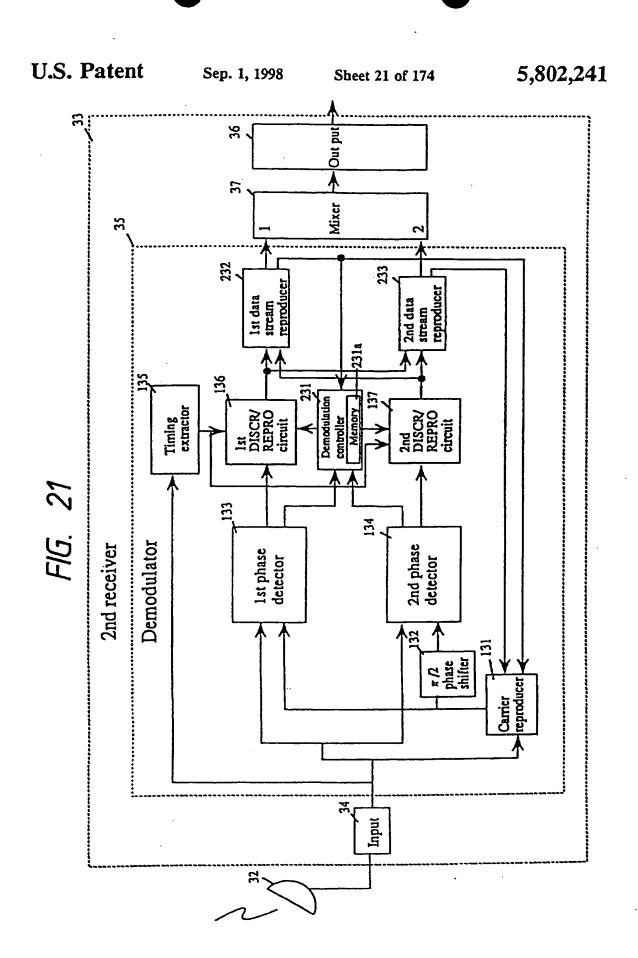


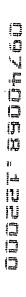


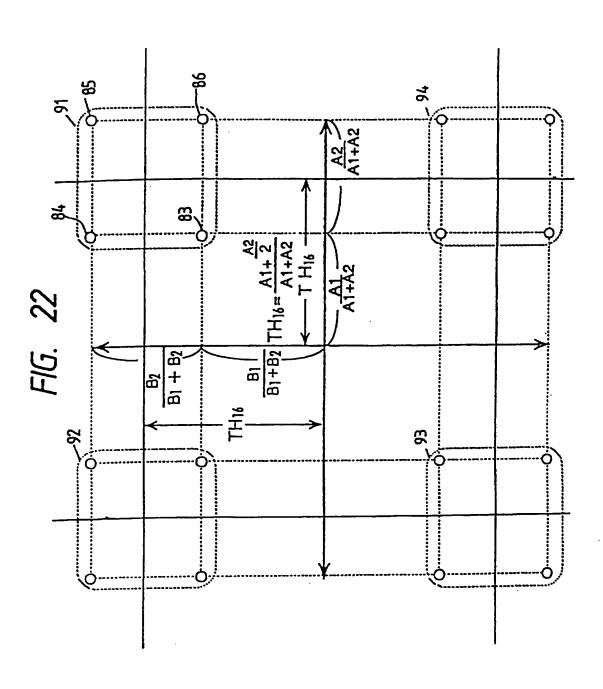
Sheet 20 of 174

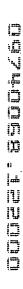
5,802,241

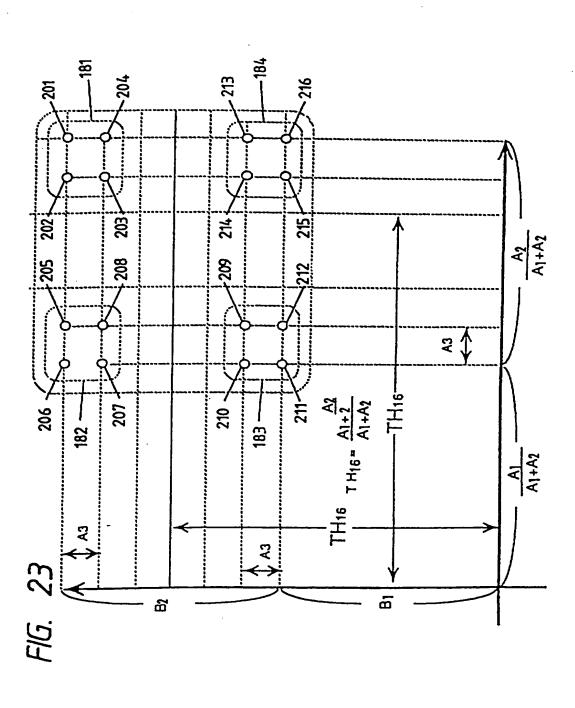






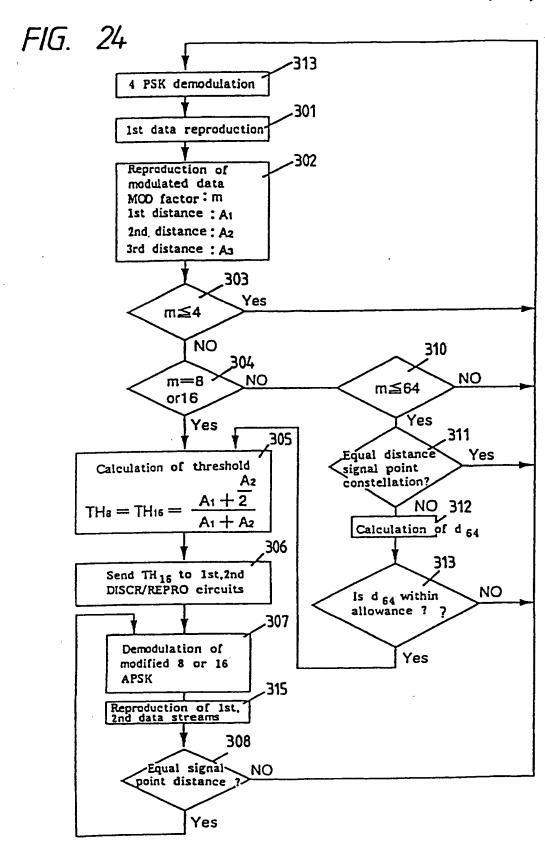






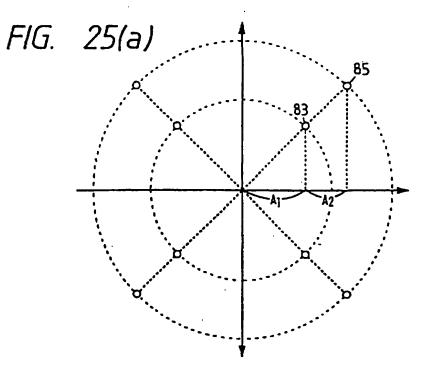
Sep. 1, 1998

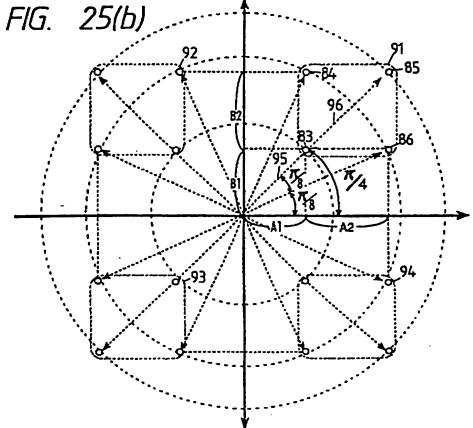
Sheet 24 of 174

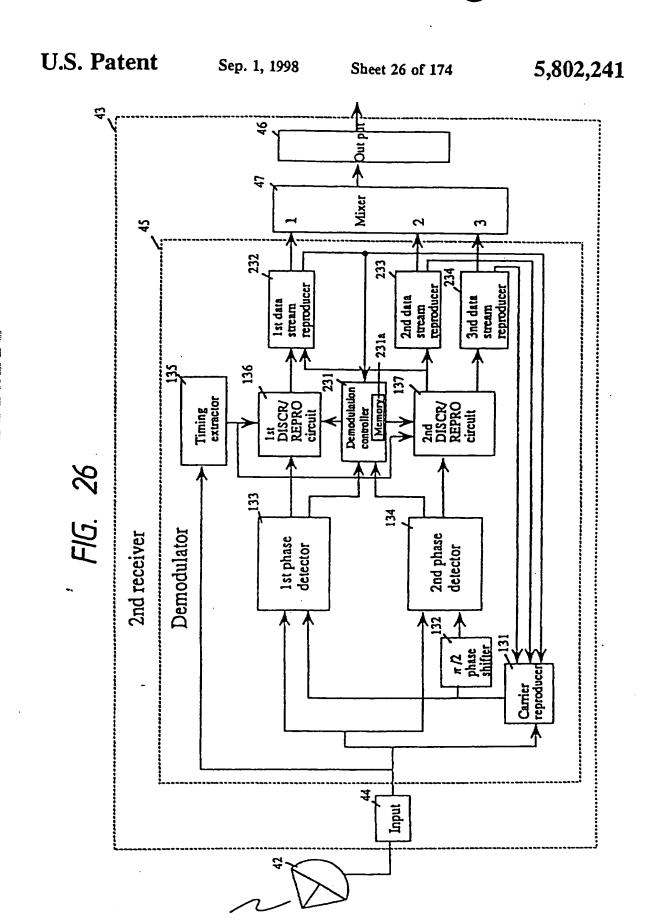


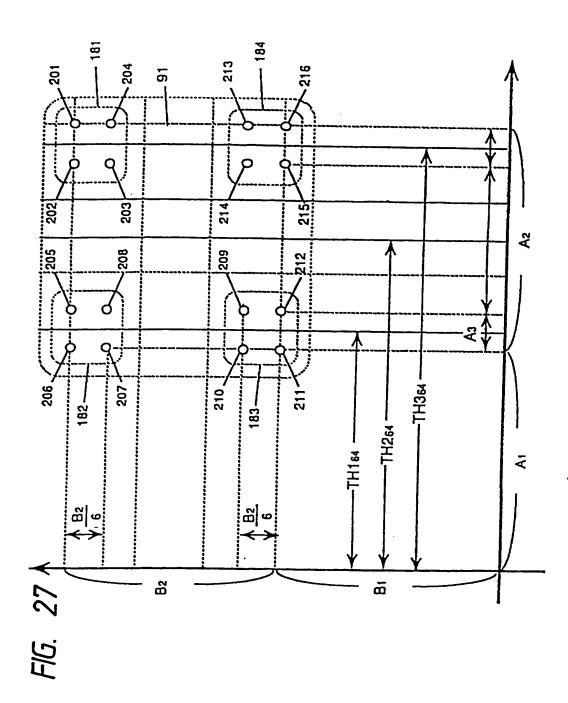
**U.S.** Patent Sep. 1, 1998

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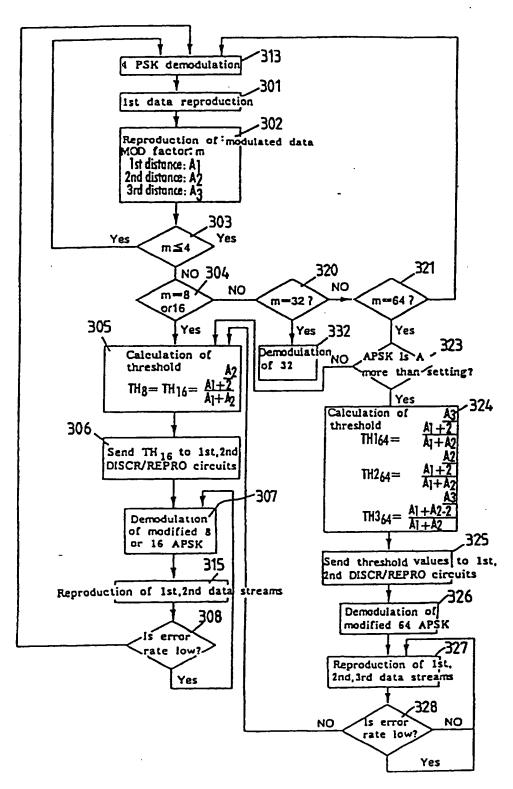




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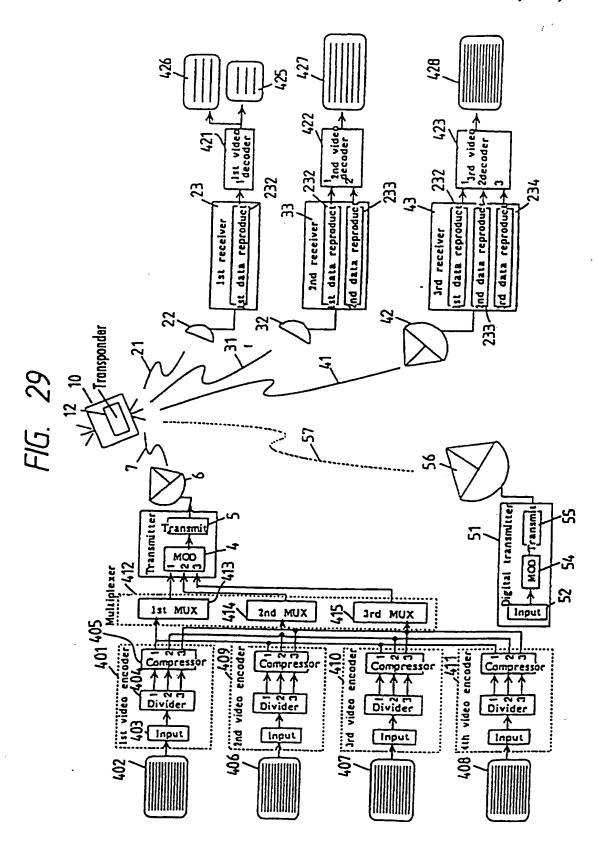
Sheet 28 of 174

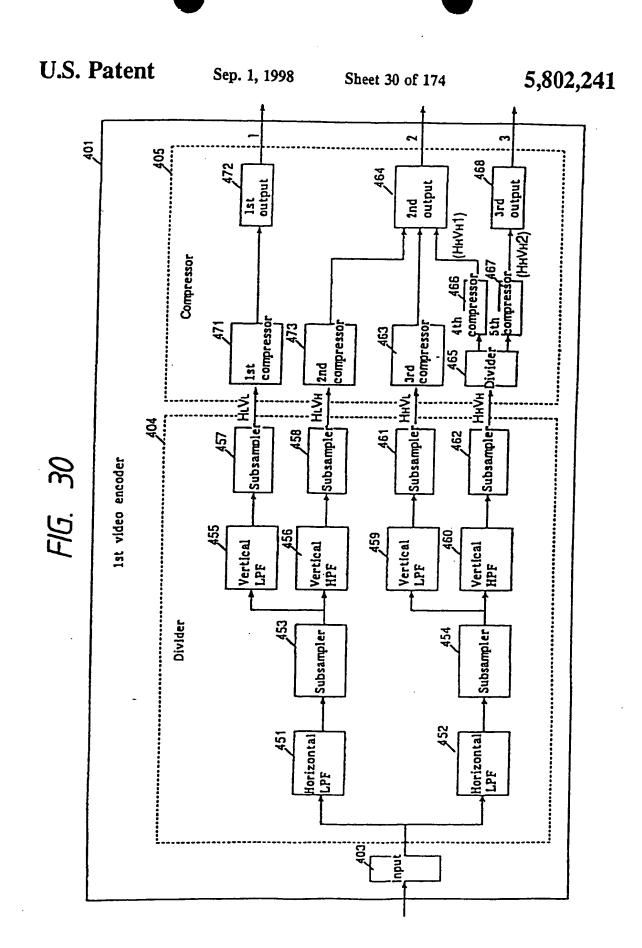
FIG. 28



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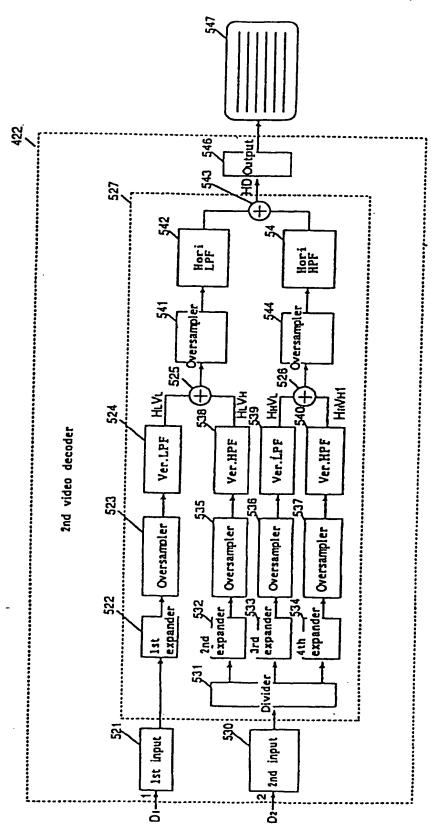
U.S. Patent Sep. 1, 1998 Sheet 31 of 174 5,802,241 508 Output 504 1st video decoder 503 Descrambler 501 ō



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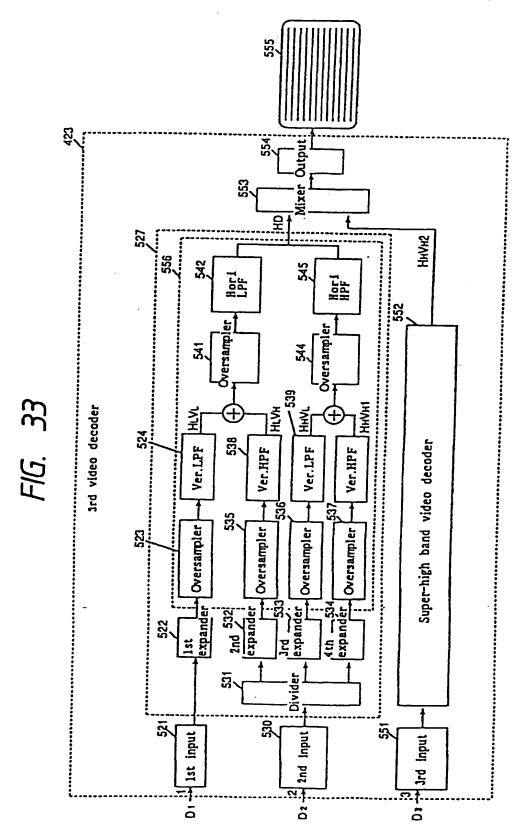


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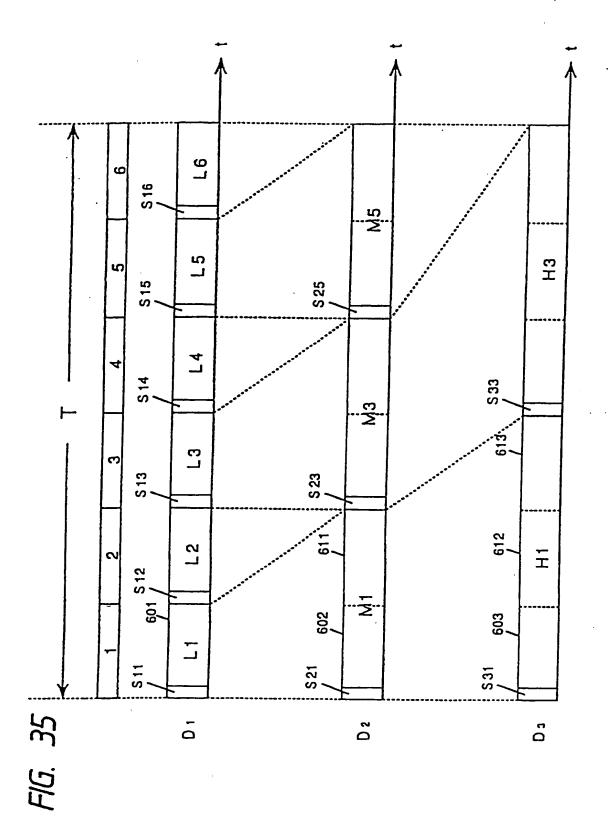
U.S. Pa	atent		Sep. 1, 1998	Sheet 1	34 of 174	5.80	2,241
			<b>-</b> -	•	<u>.</u>	2,00	- -
				,			
	9	L6		M6		9H	
	5	L5	·	M5		HS	
	4	L 4		<b>A</b>		H4	
	က	L3		M3	***************************************	E H	
	2	L2	·	M2		H2	
	1 691	L1		M 1		H1 603	
FIG. 34		ā		٥		G G	



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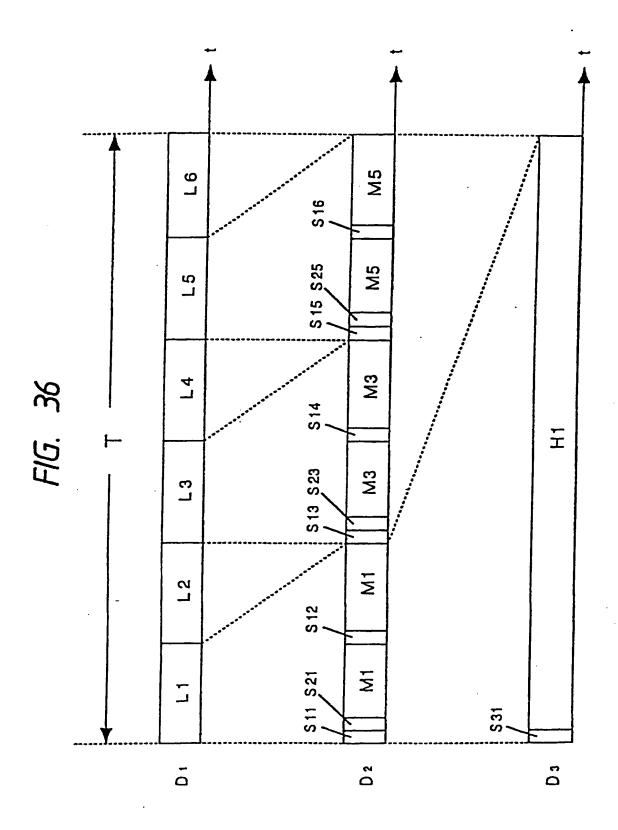
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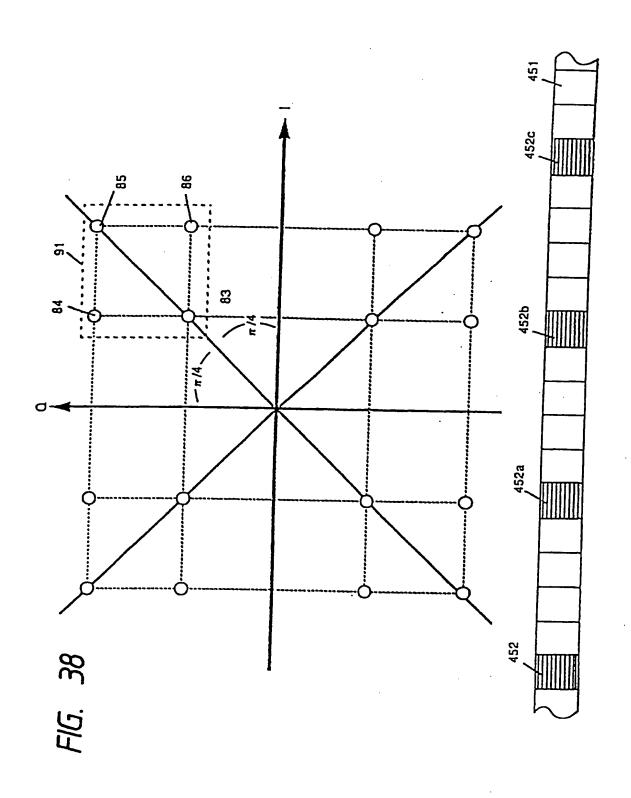


reproduct 2nd receiver T<u>ransmit</u> 2nd MUX Ird MUX Compressor 408 4th video encoder 3rd video encoder Divider Divider Divider Input Input Input

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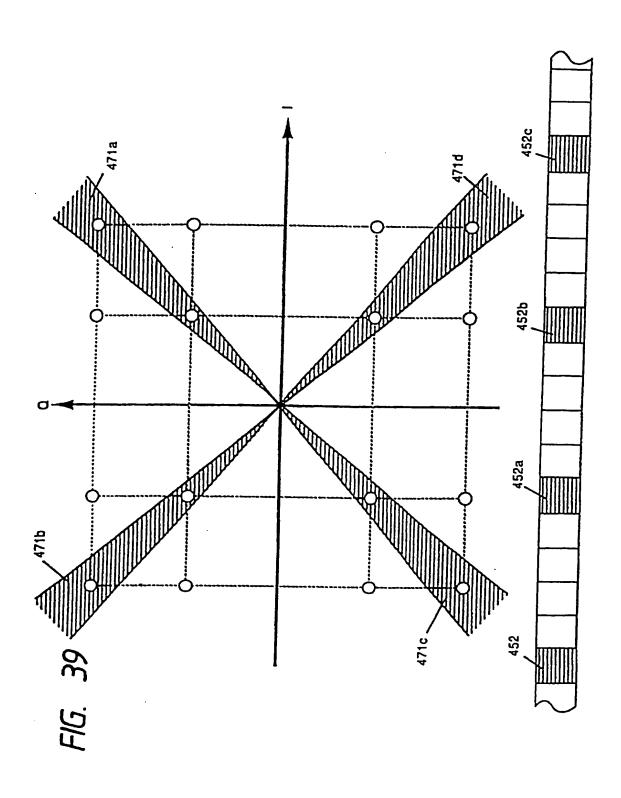
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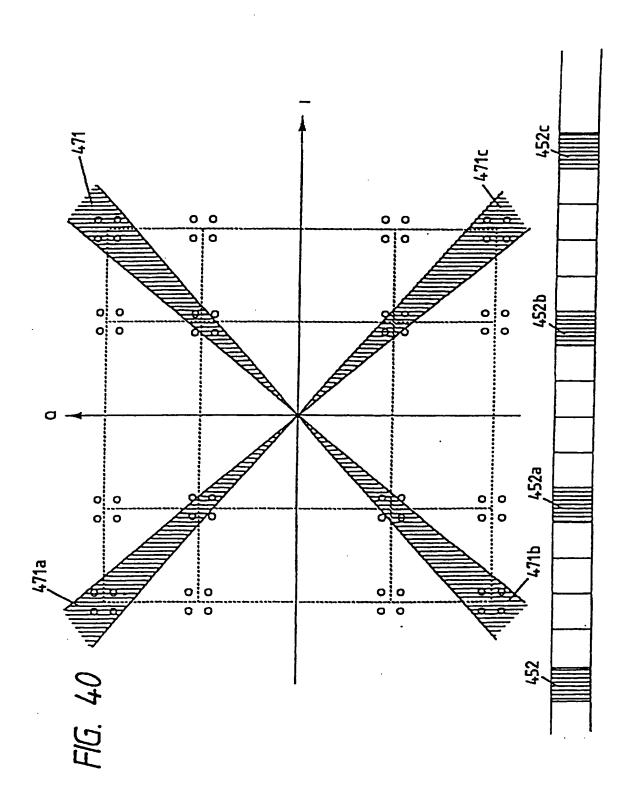




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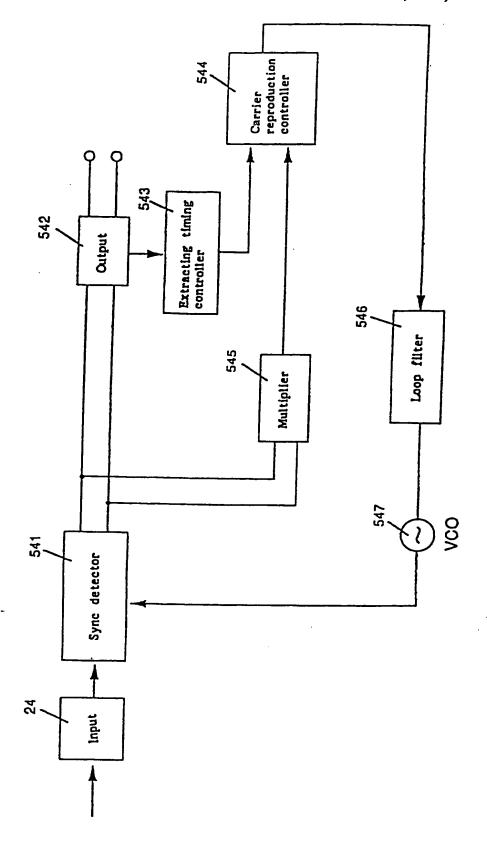
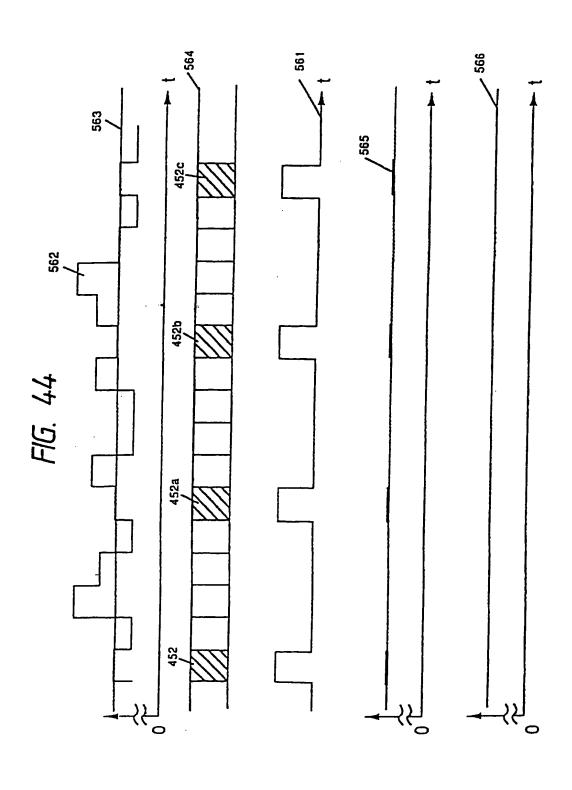


FIG. 43



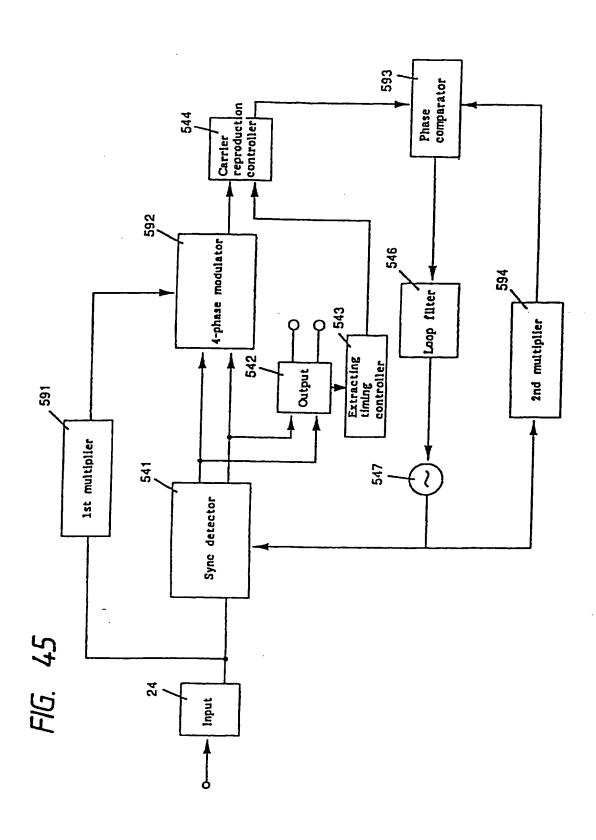
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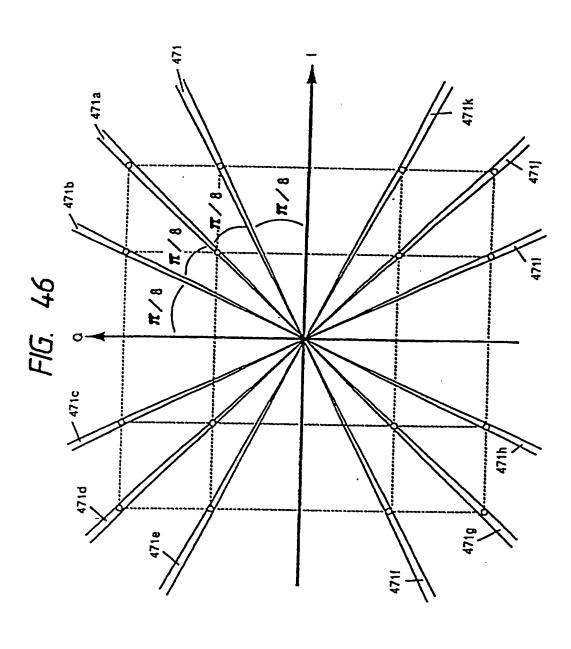


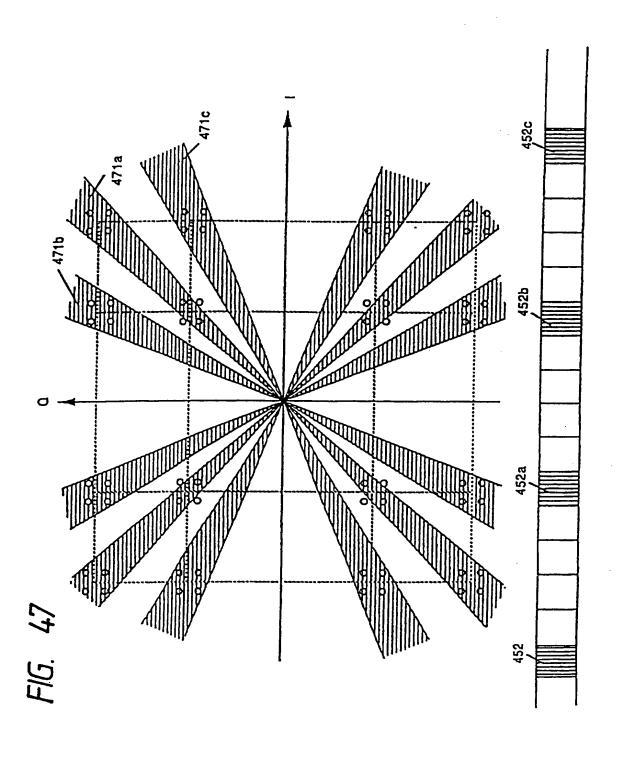
Sep. 1, 1998

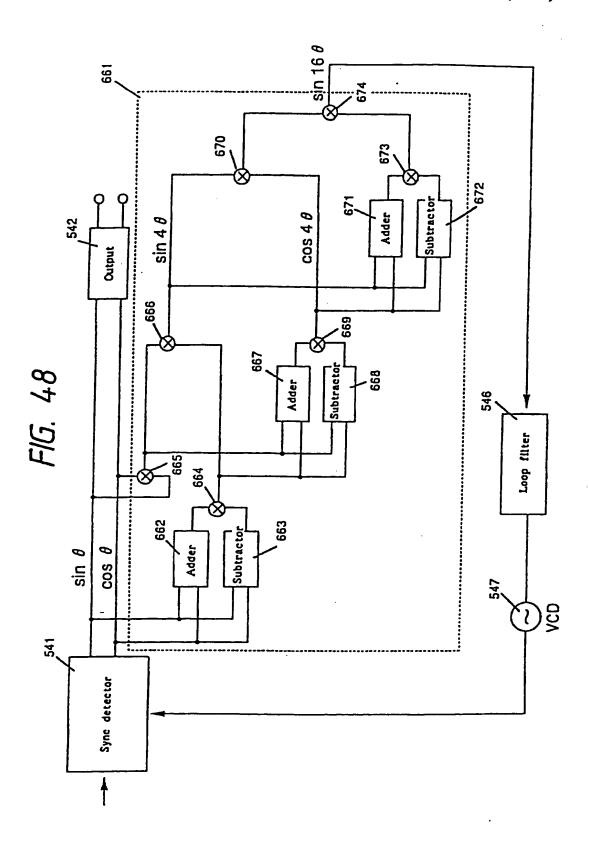
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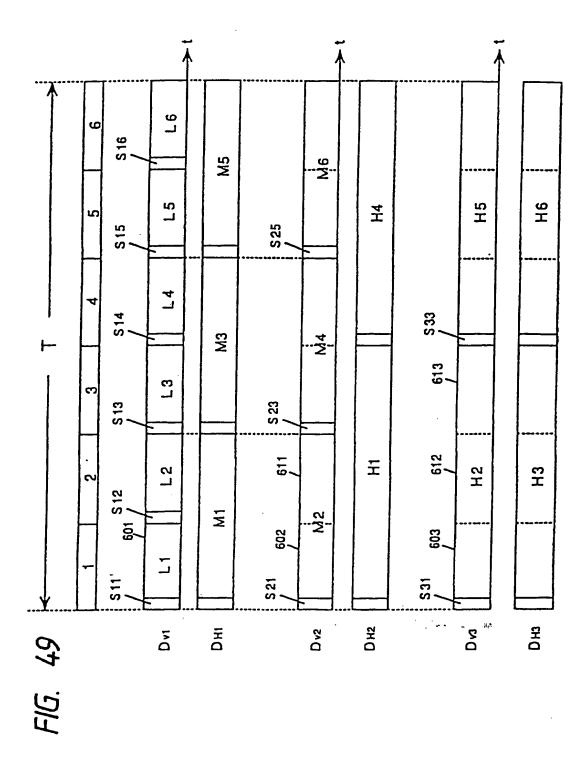
5,802,241



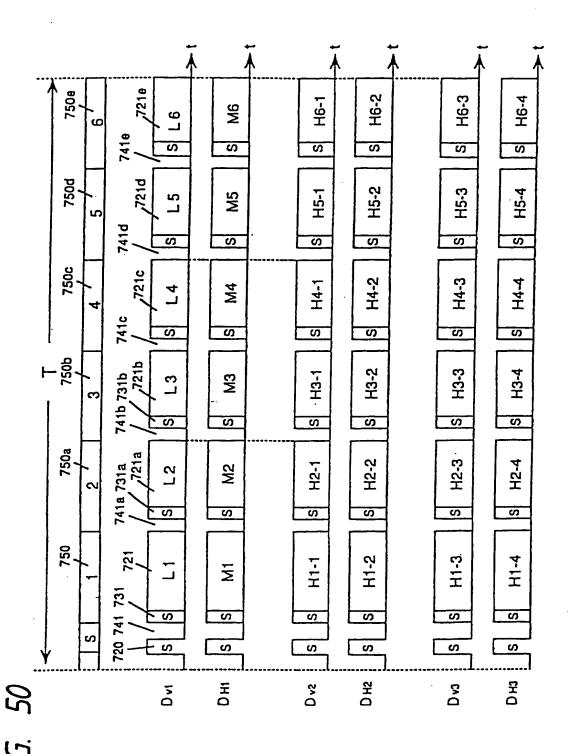








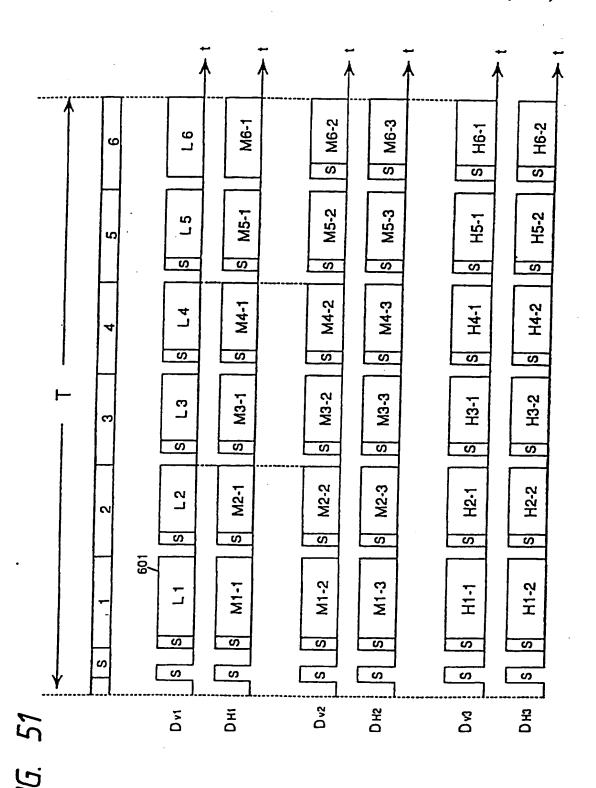
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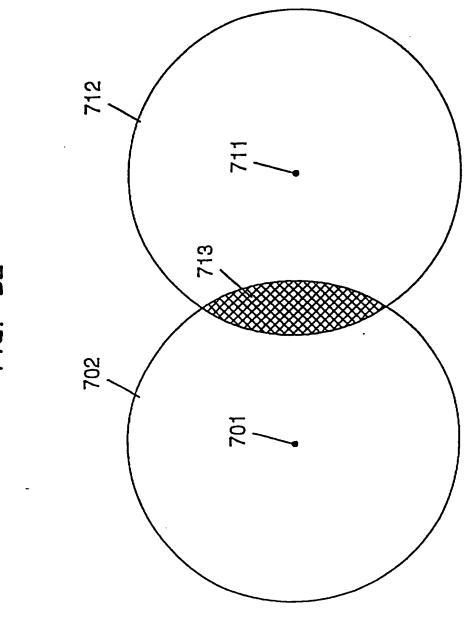




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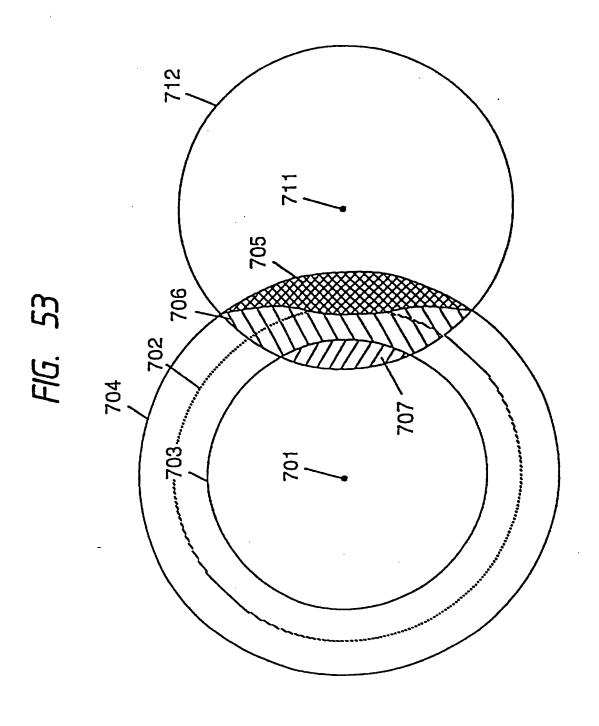
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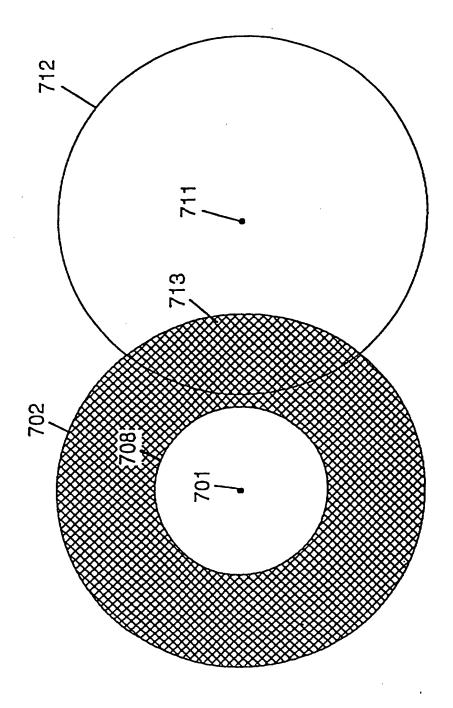
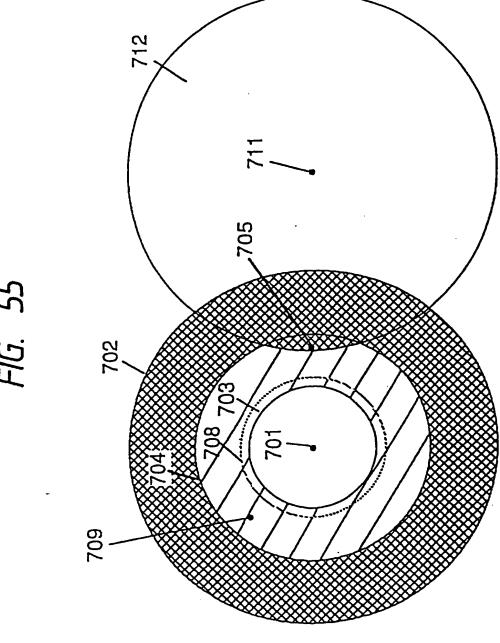


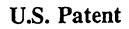
FIG. 54



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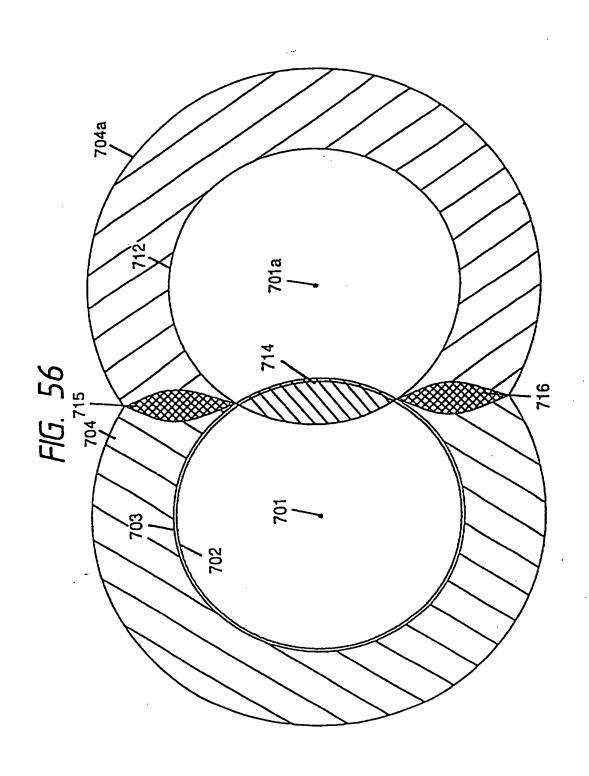


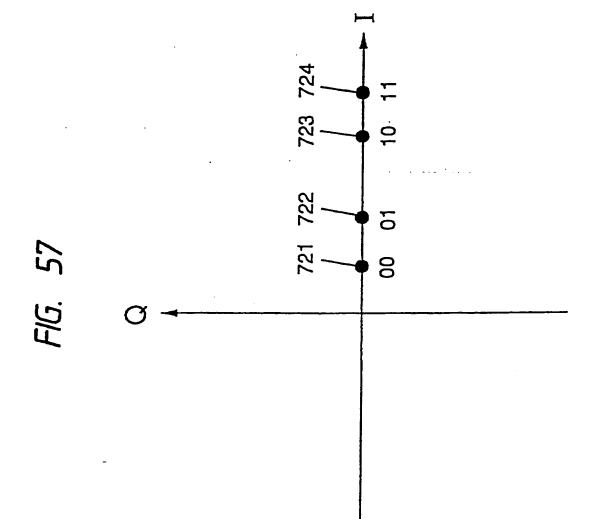


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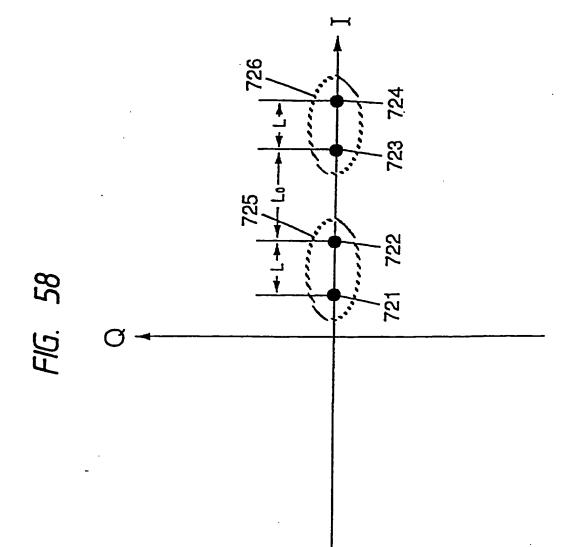
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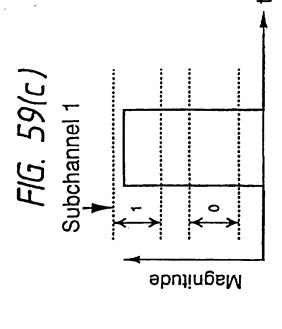
5,802,241

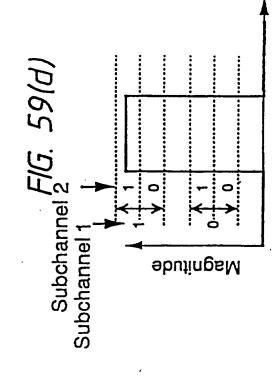


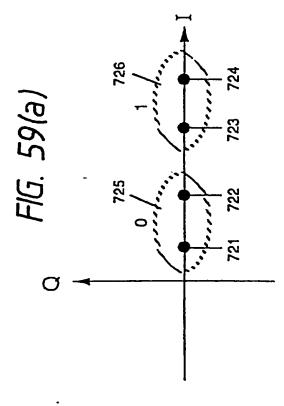


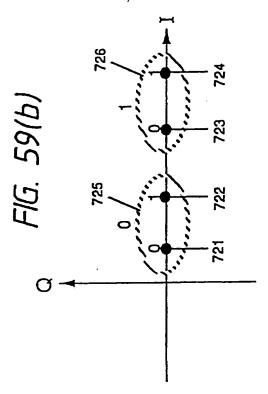
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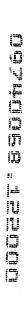


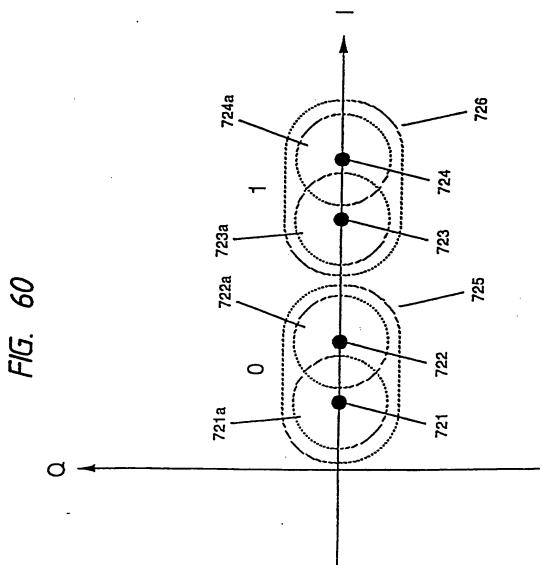


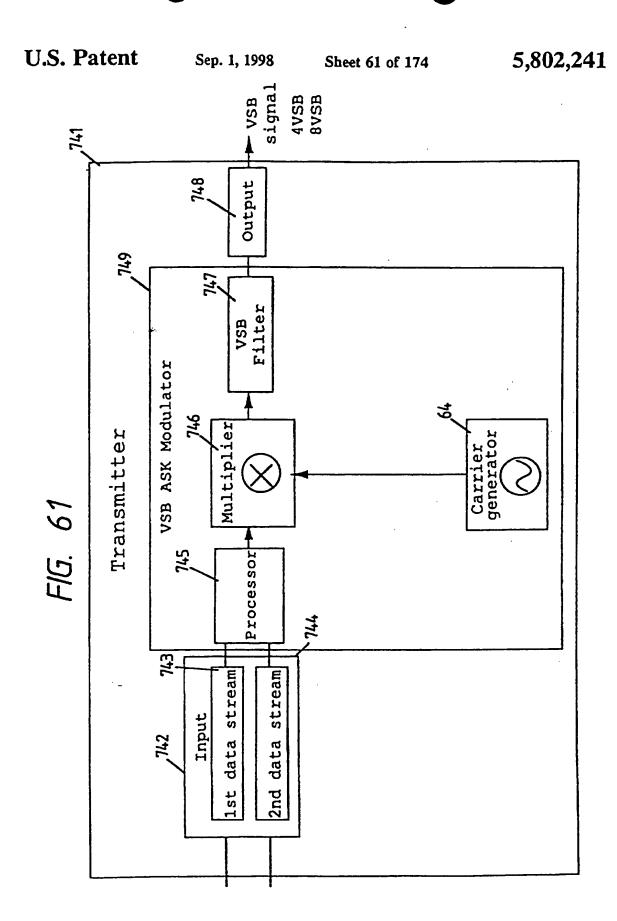


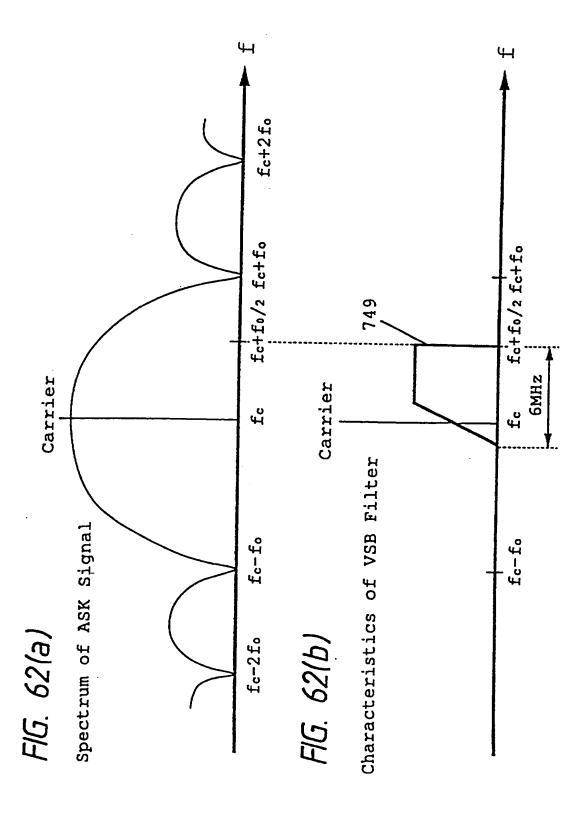










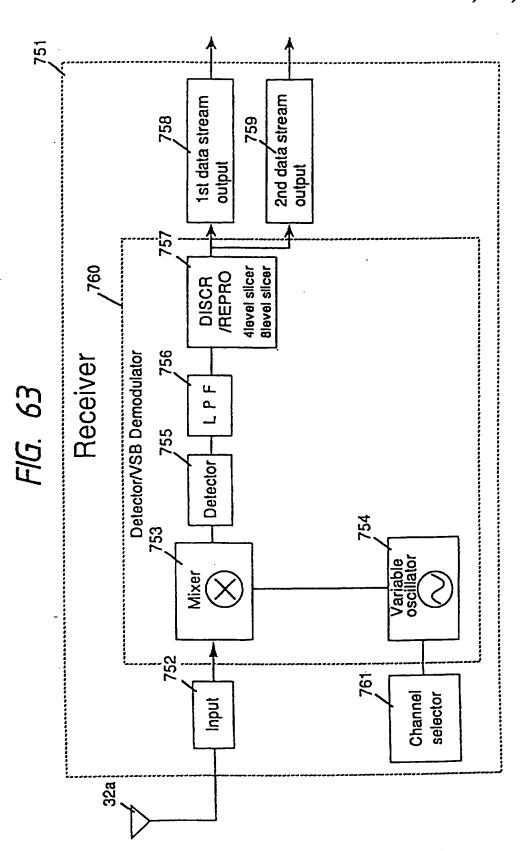


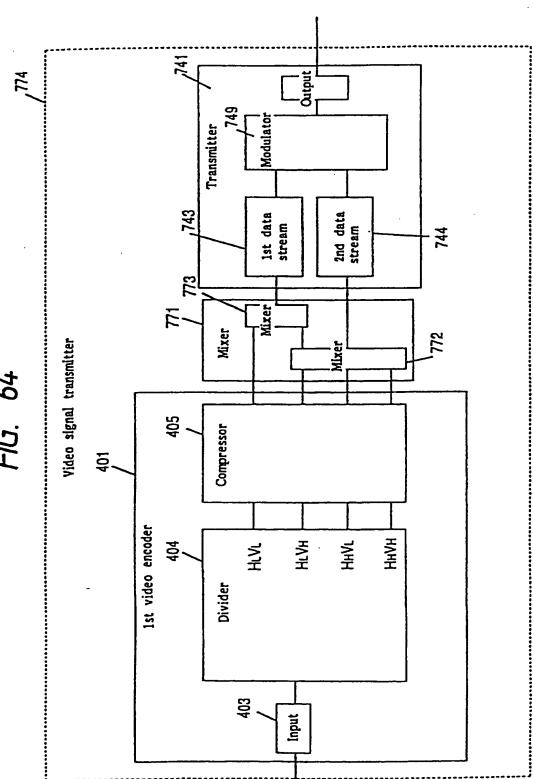


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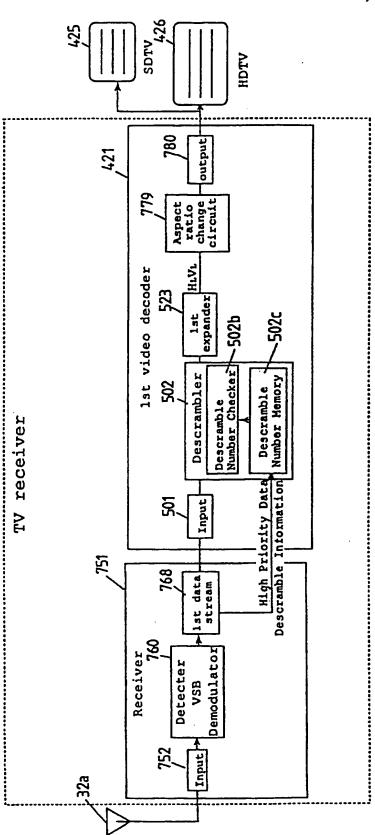
5,802,241

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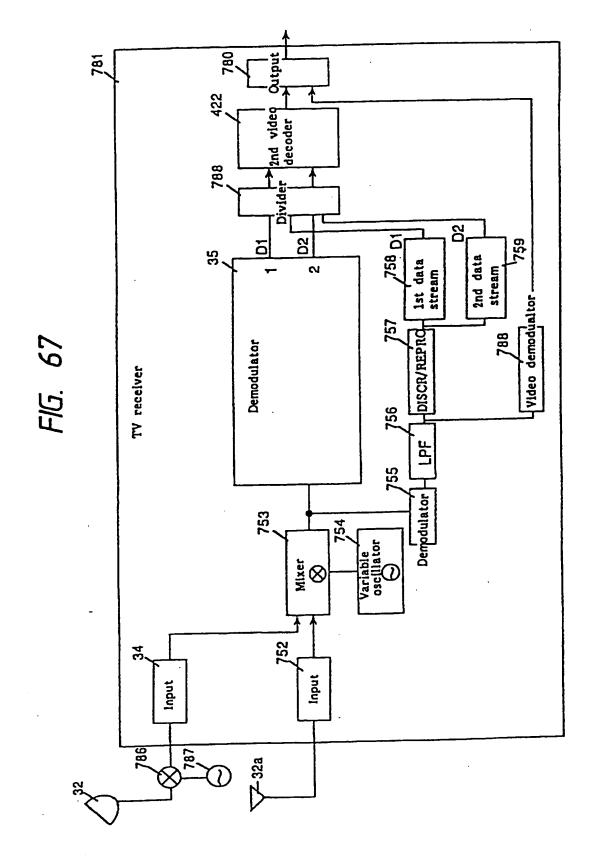
Sep. 1, 1998

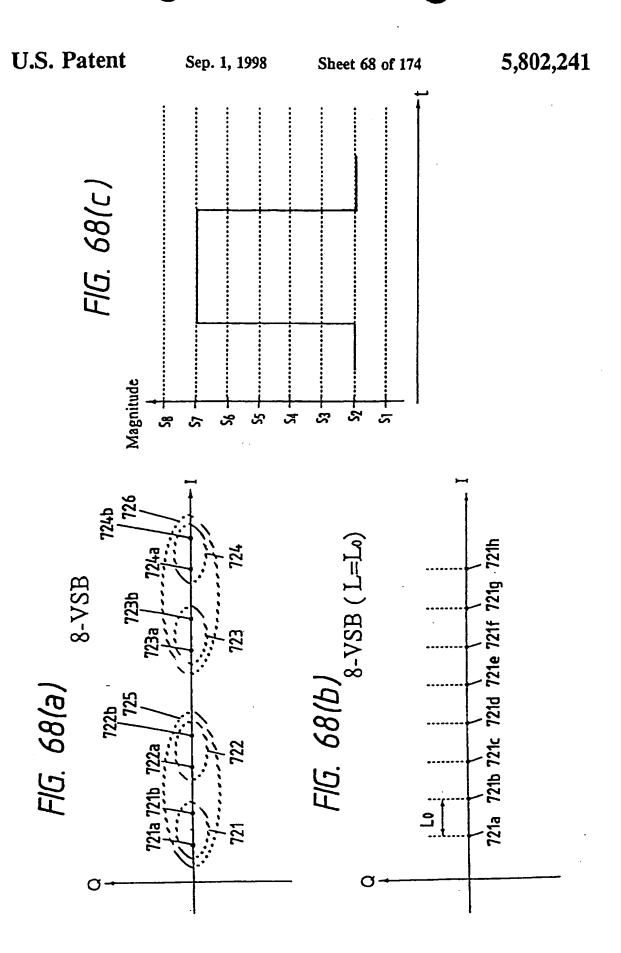
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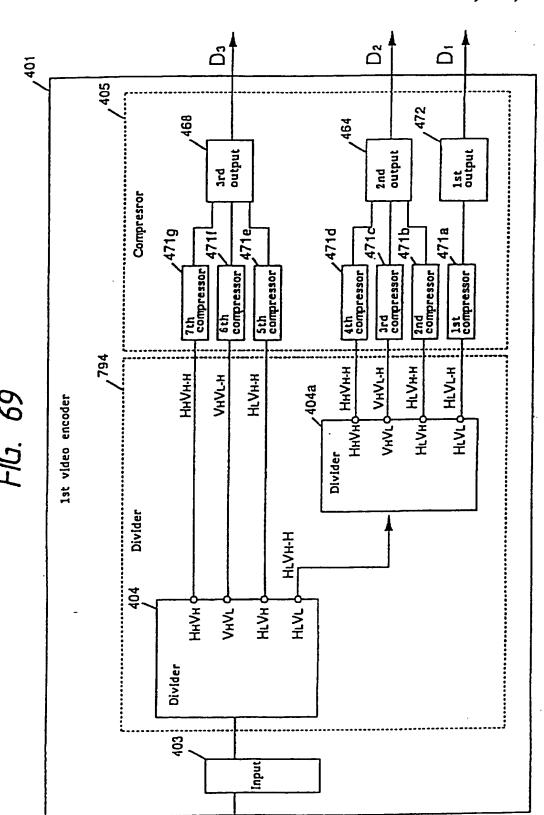
5,802,241

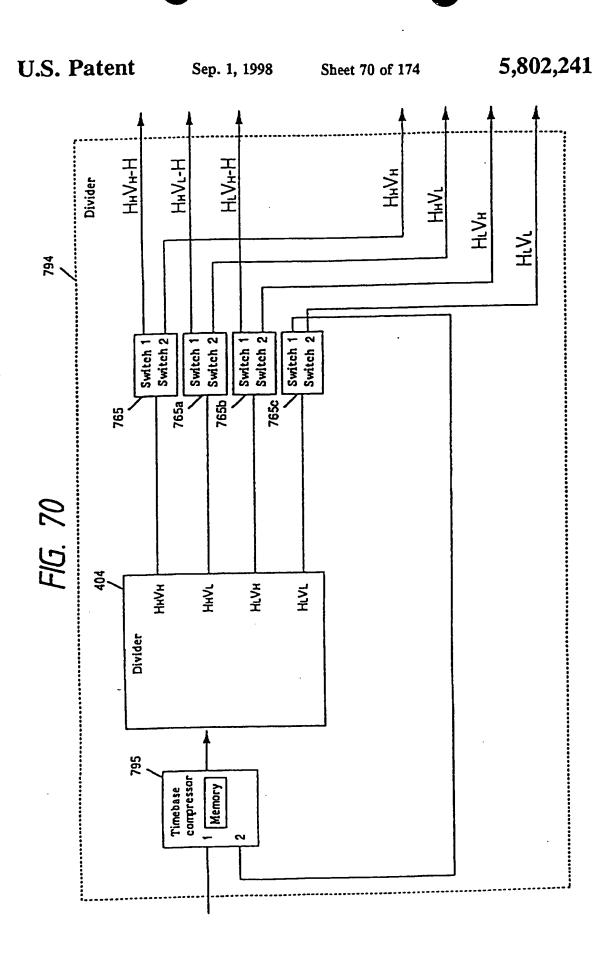


:1*G*: 66











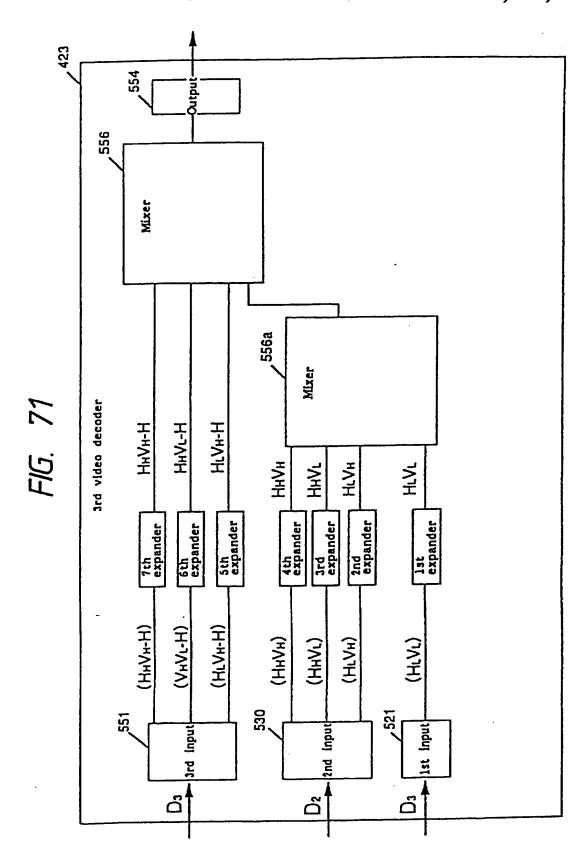


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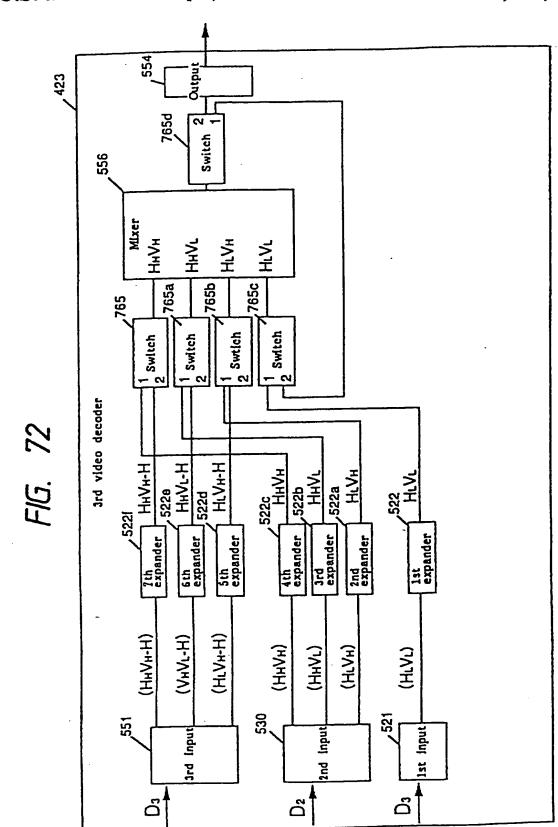
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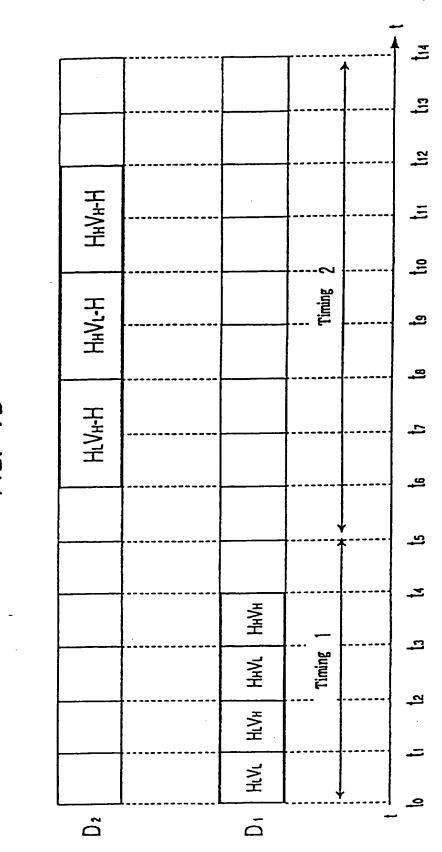
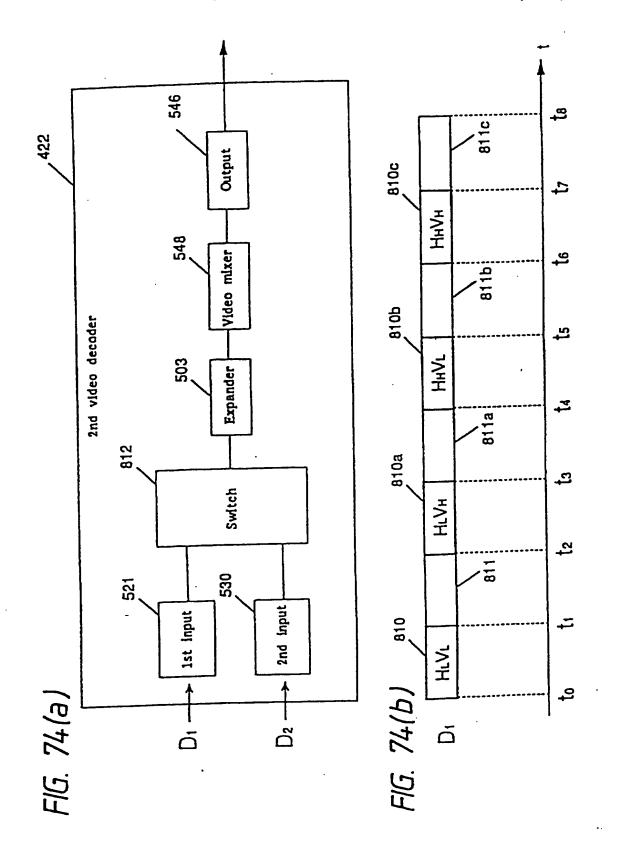
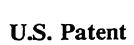


FIG 73

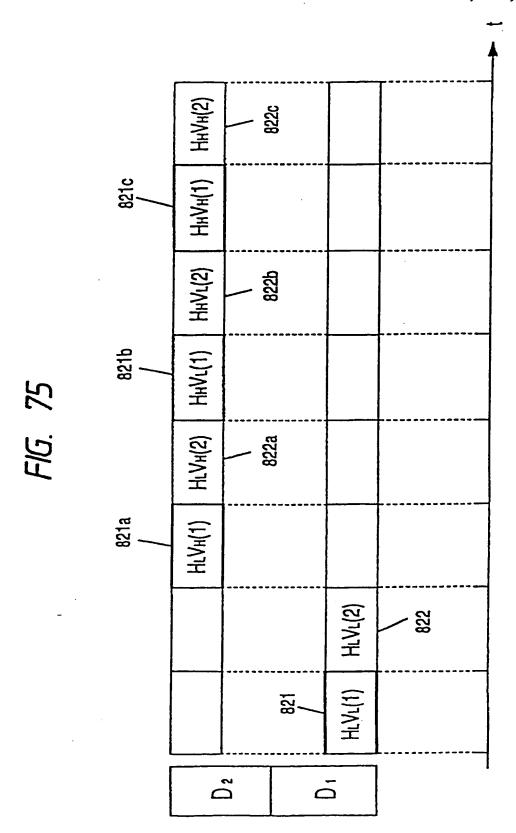




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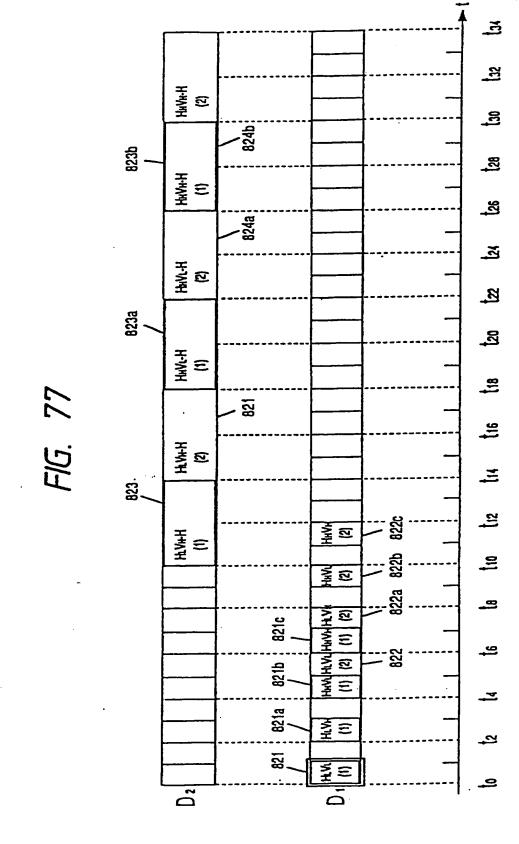


U.S. Patent 5,802,241 Sep. 1, 1998 Sheet 76 of 174 Ни/н(2) 822c 821c **Н**нVн(1) HHV<sub>L</sub>(2) 822b 821b HHVL(1) 01-2 FIG. 76 822a H<sub>1</sub>V<sub>H</sub>(2) 821a H.VH(1) HrVr(2) 822 더:1

821

H.V.(1)

ECC



U.S. Patent Sep. 1, 1998 5,802,241 Sheet 78 of 174 521 521 765a Switch 556 Mixer 품 H\_VH HEV 765 Switch 2 3rd video encoder 812a 503 Expander Switch 521 530 Input Input

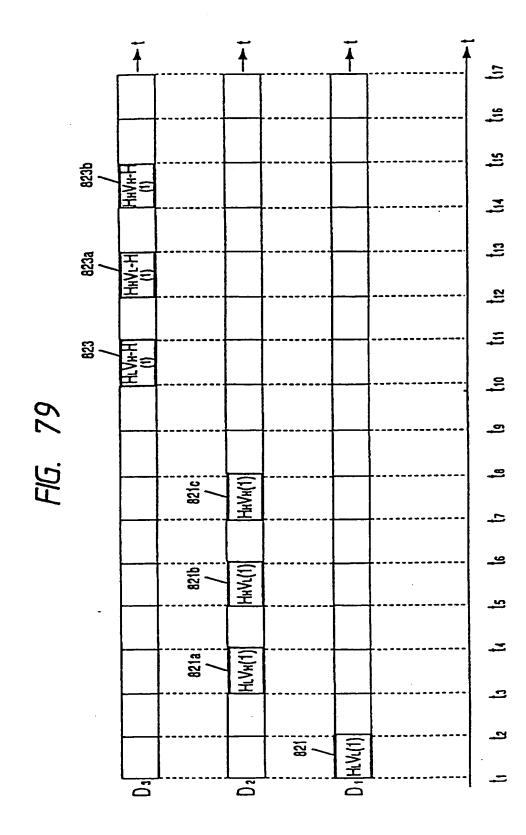
> HHVH-H HHVL-H HLVH-H

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5,802,241

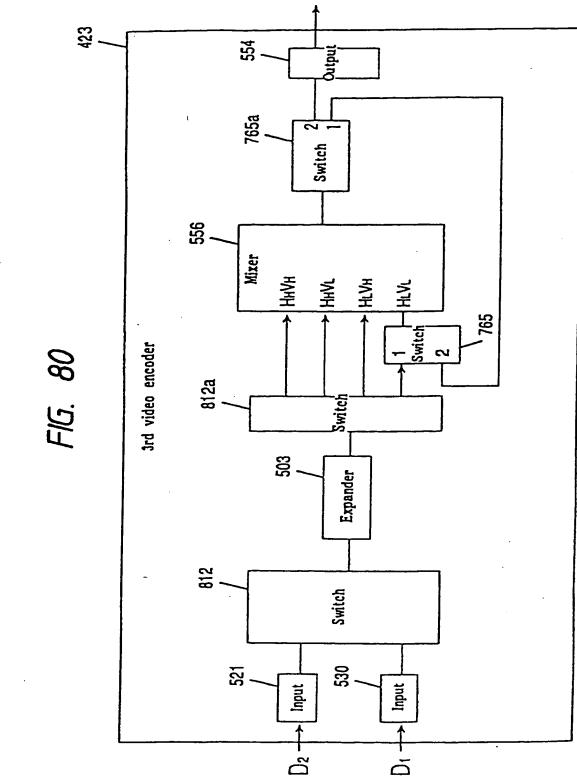




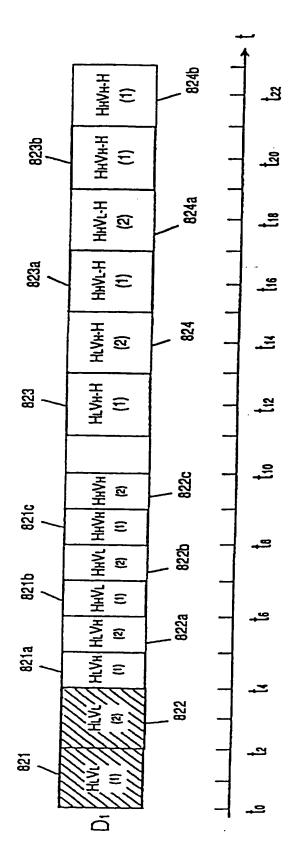
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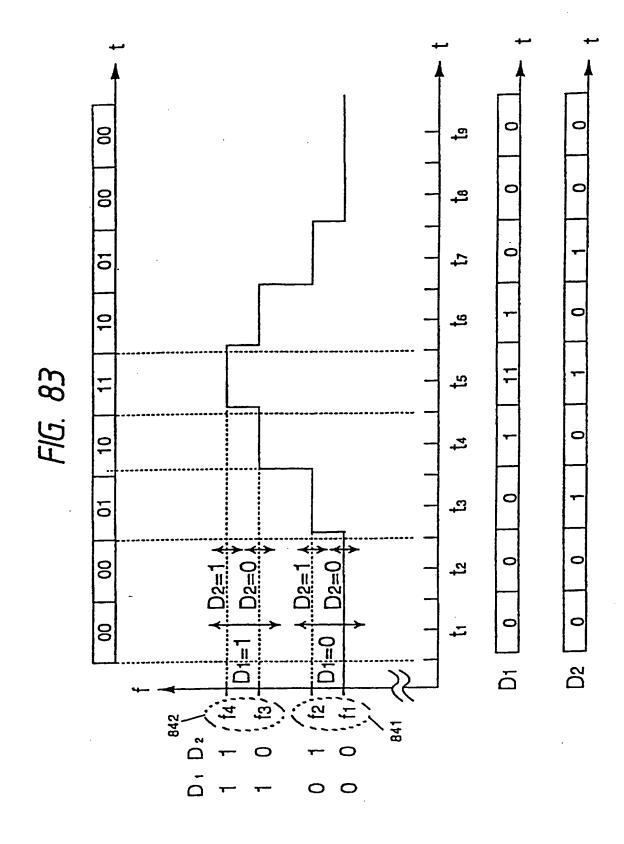
5,802,241



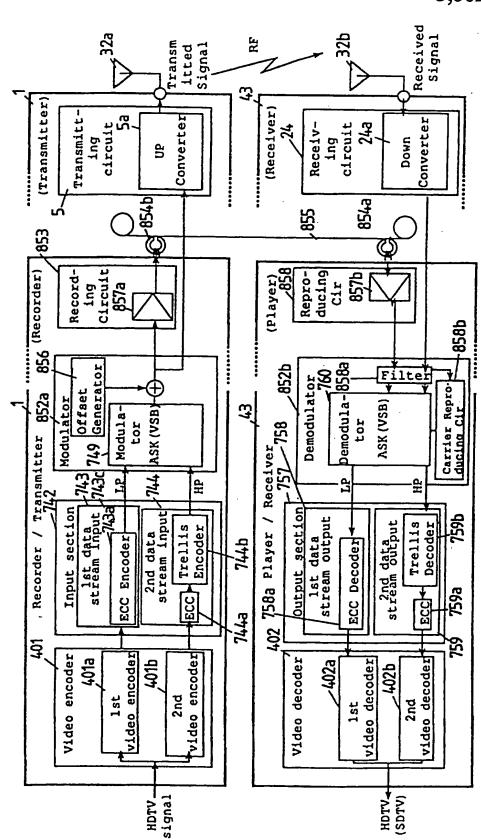




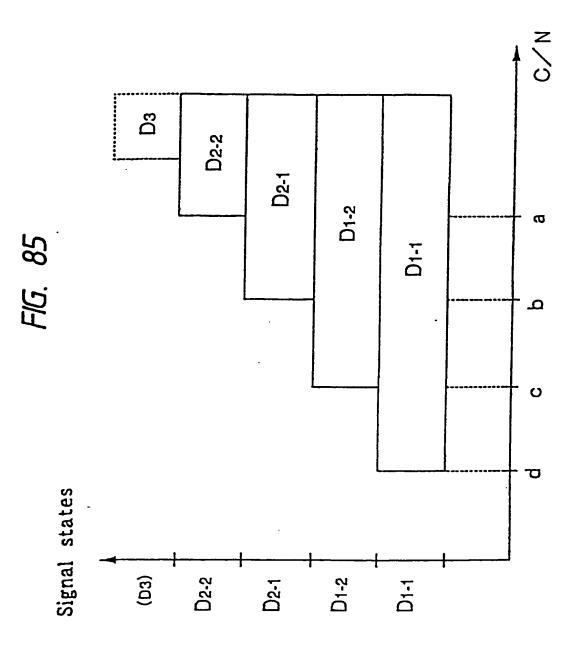
U.S. Patent Sep. 1, 1998 5,802,241 Sheet 82 of 174 423 Switch 556 Mixer HHYH НгVн HH/L H.V. Switch 2 Video encoder 503 Expander Input Ō



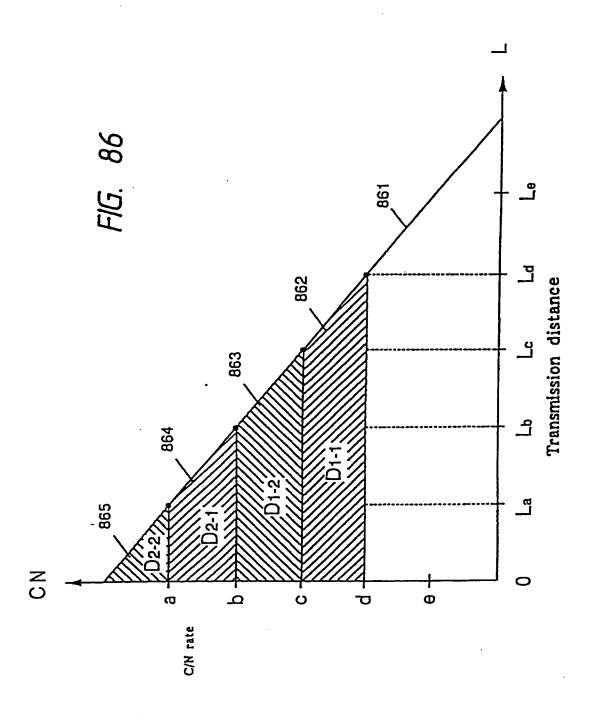
U.S. Patent

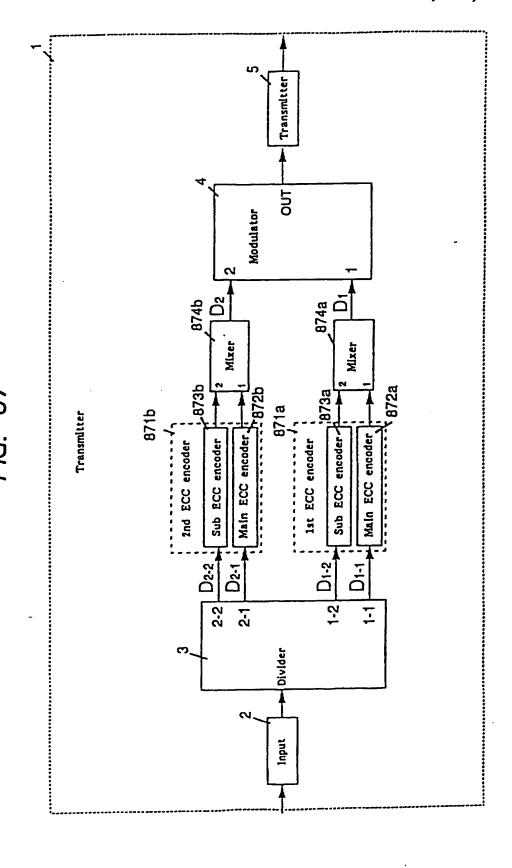


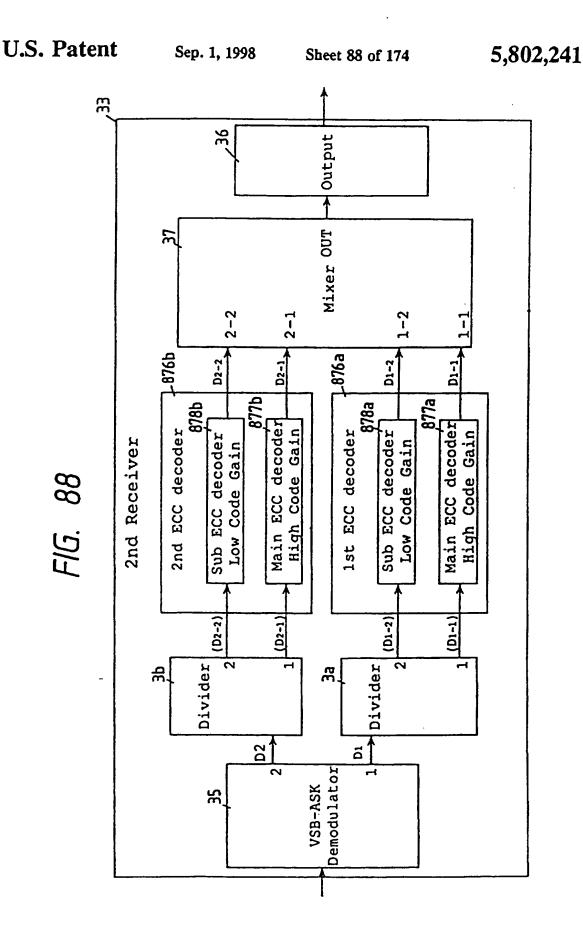
F/G. 84

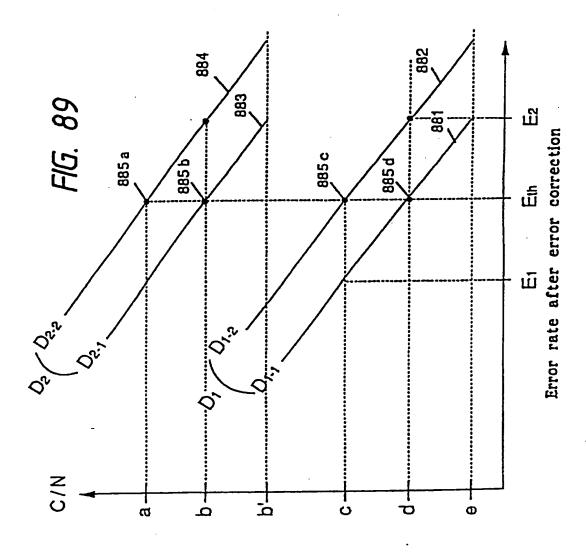


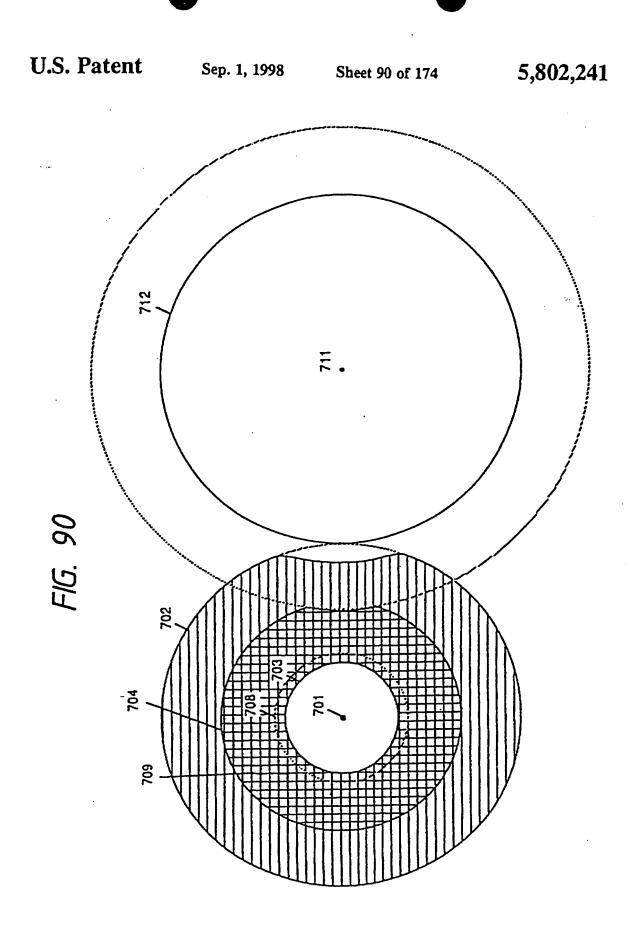


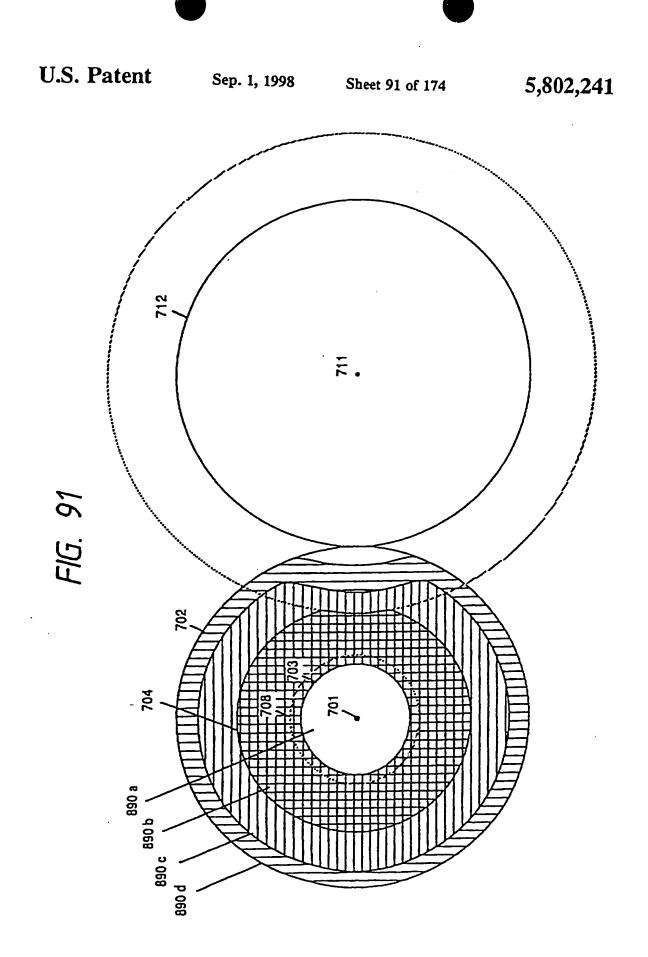










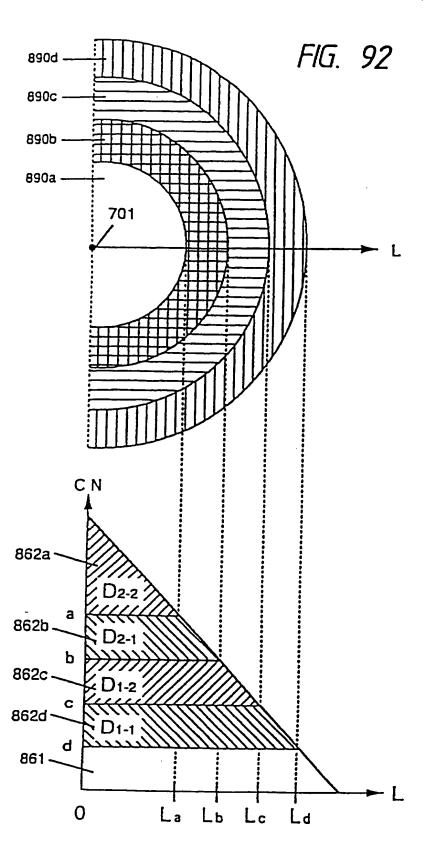


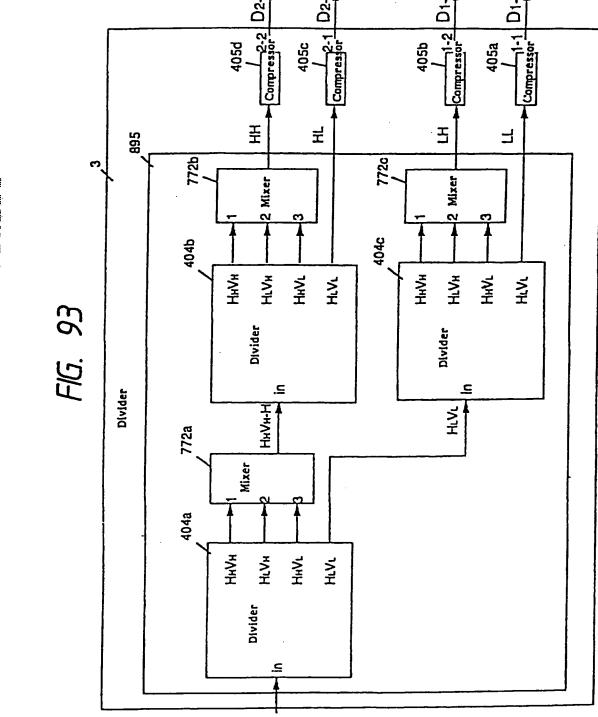


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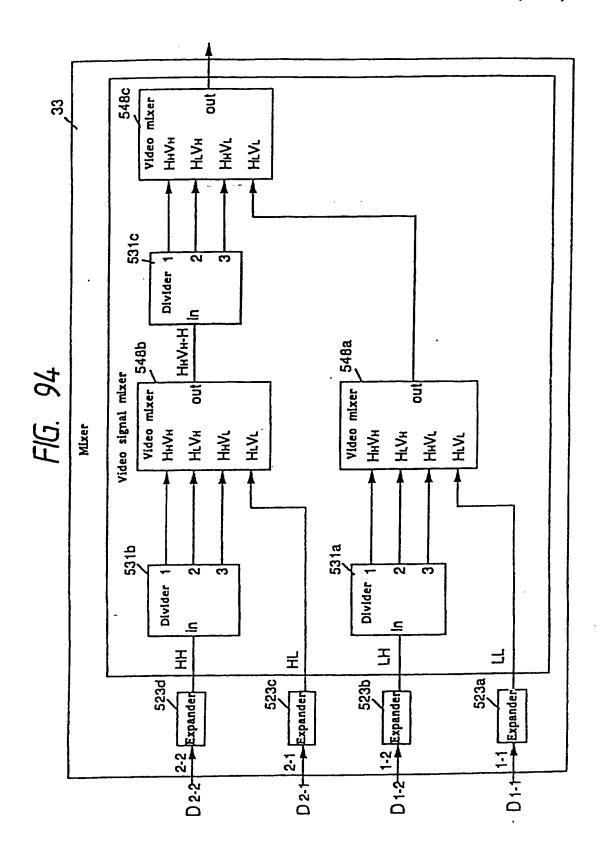
Sheet 92 of 174

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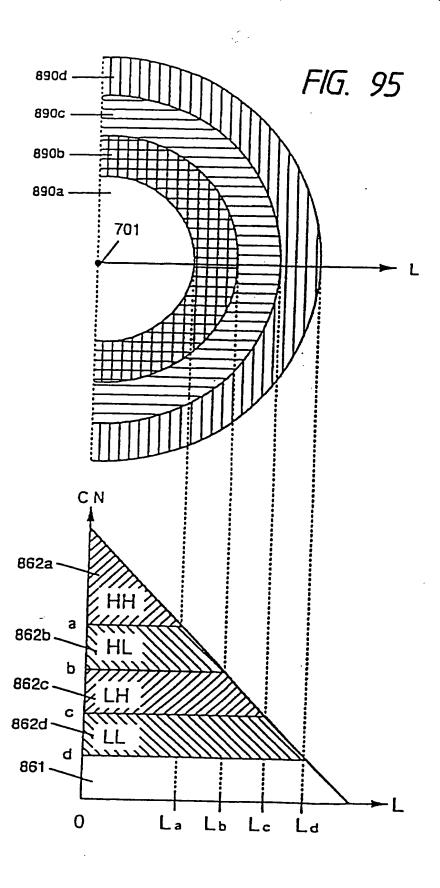




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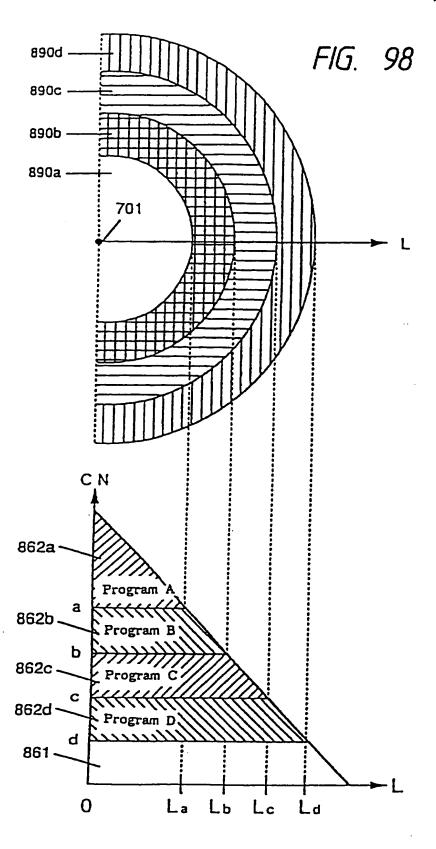
U.S. Patent Sep. 1, 1998 5,802,241 Sheet 97 of 174 Œ



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5,802,241





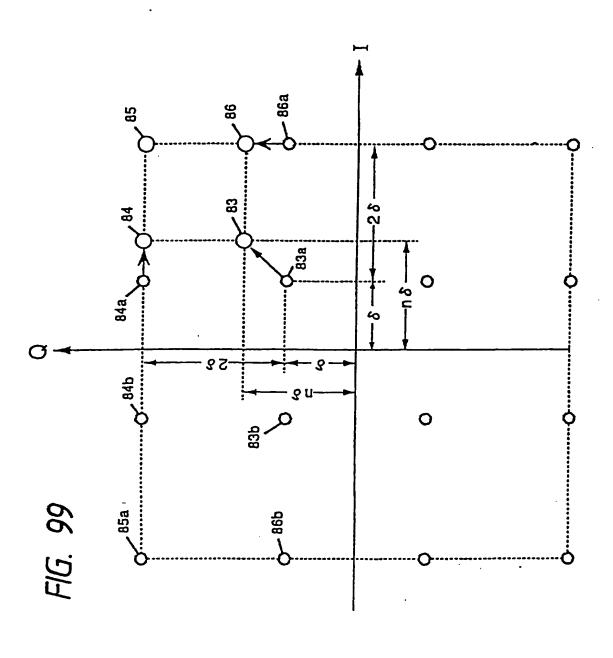
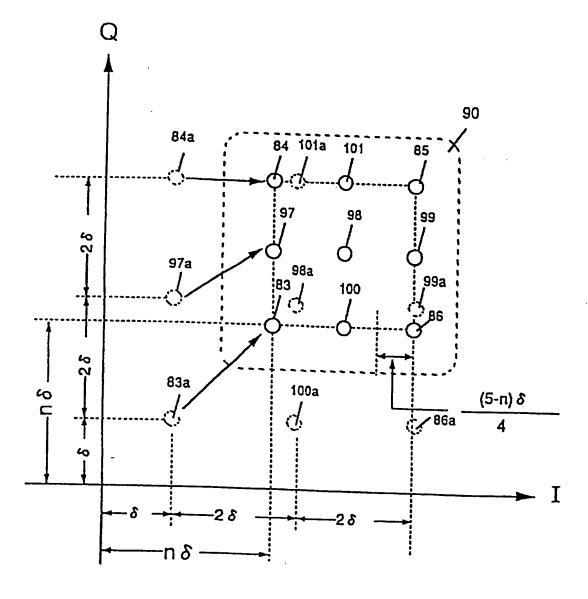


FIG. 100



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FIG. 101



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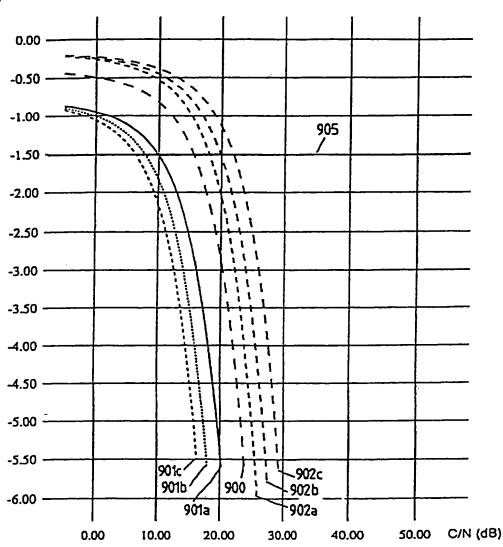
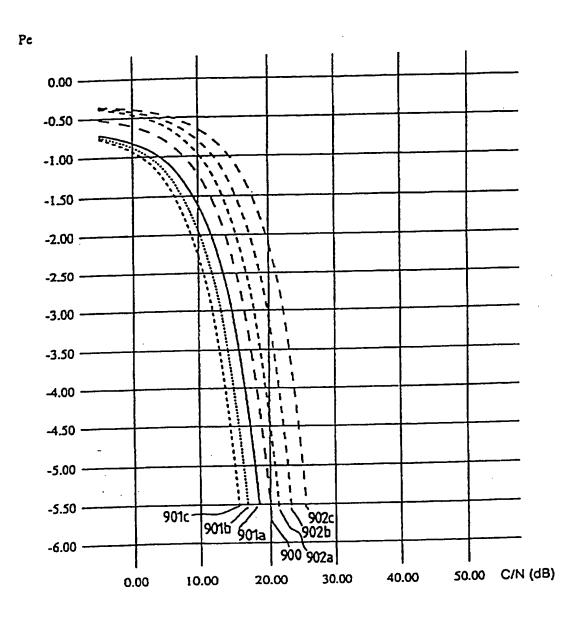
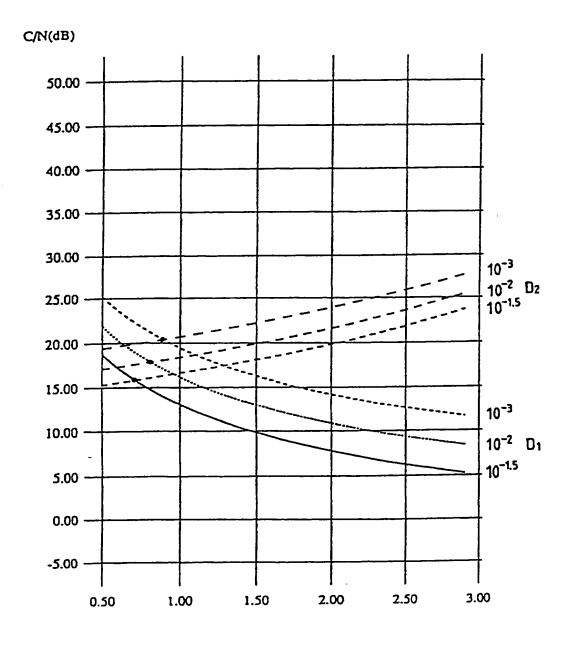


FIG. 102



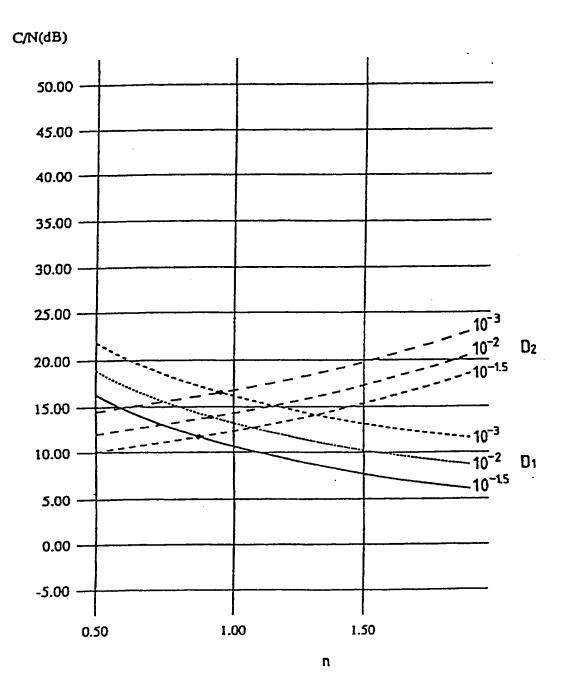
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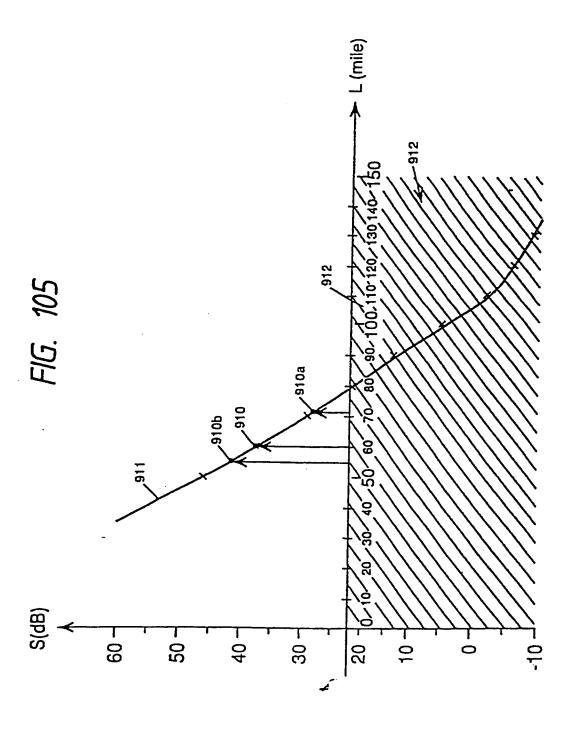
FIG. 103

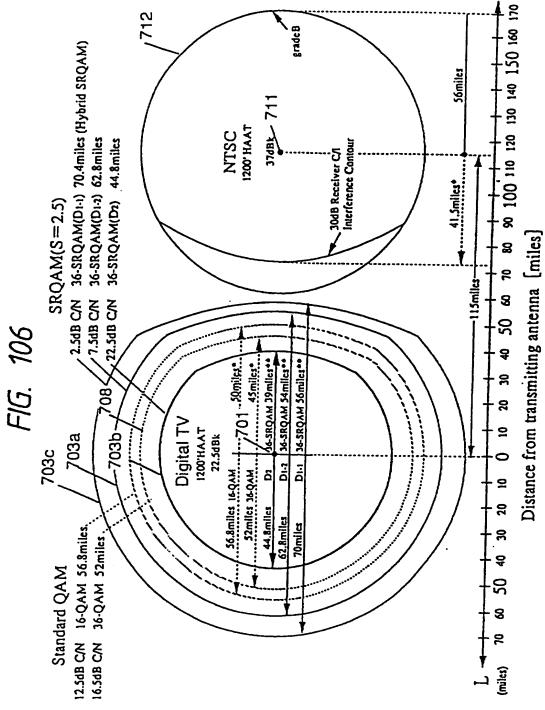


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FIG. 104

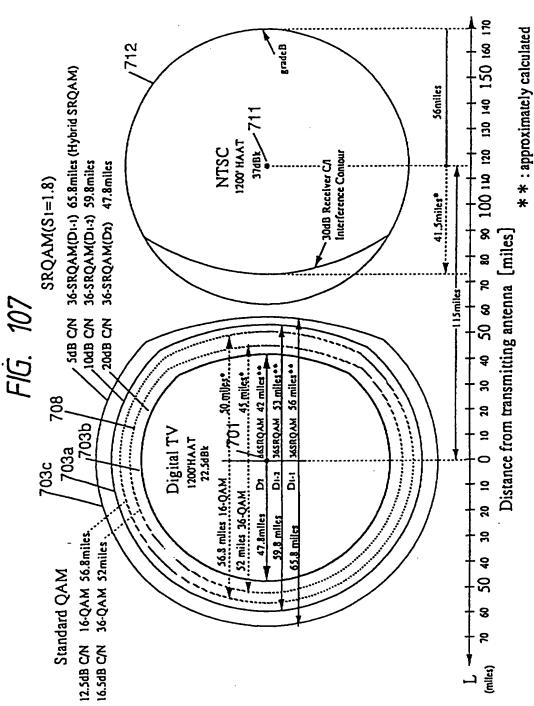


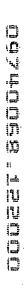


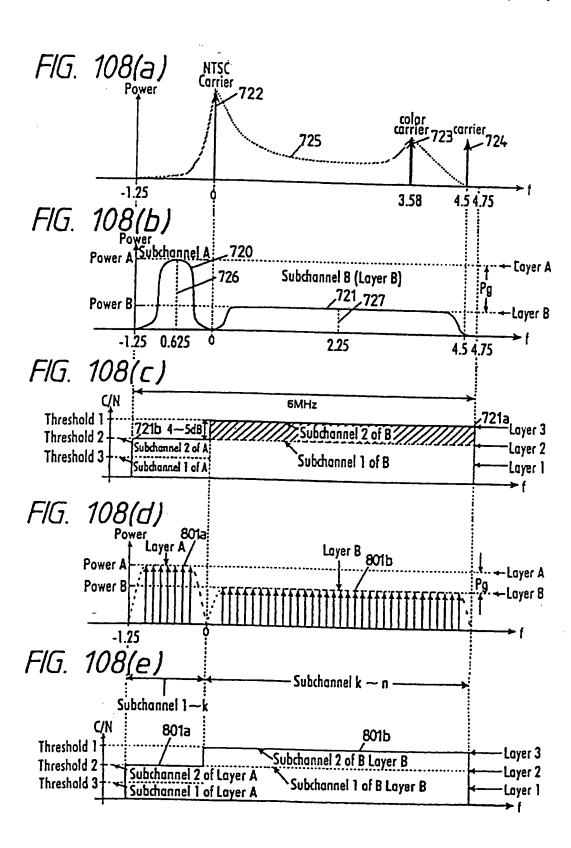


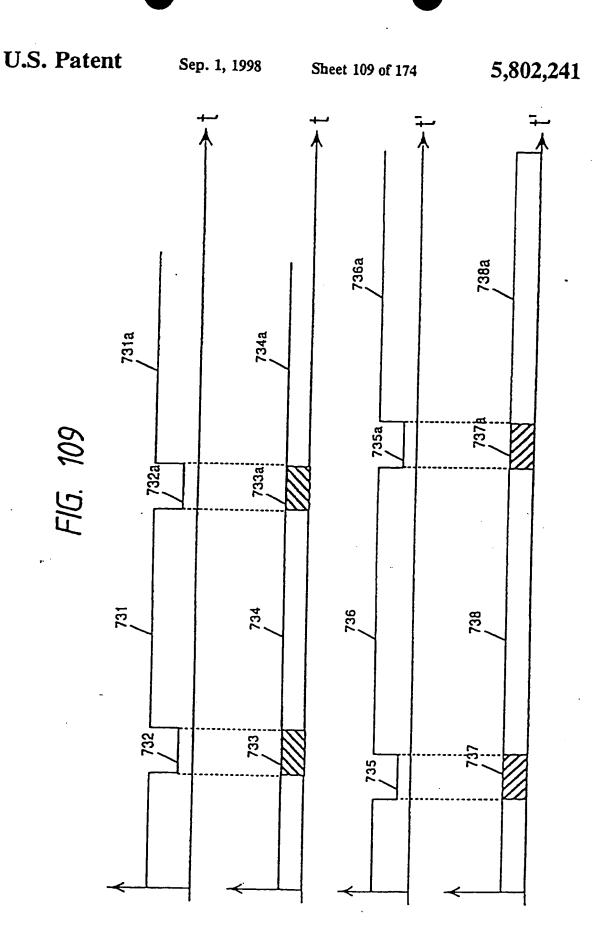
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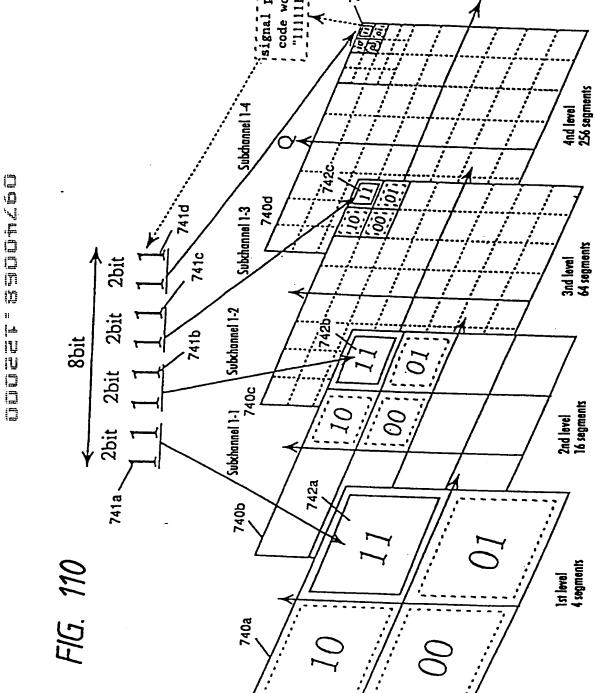
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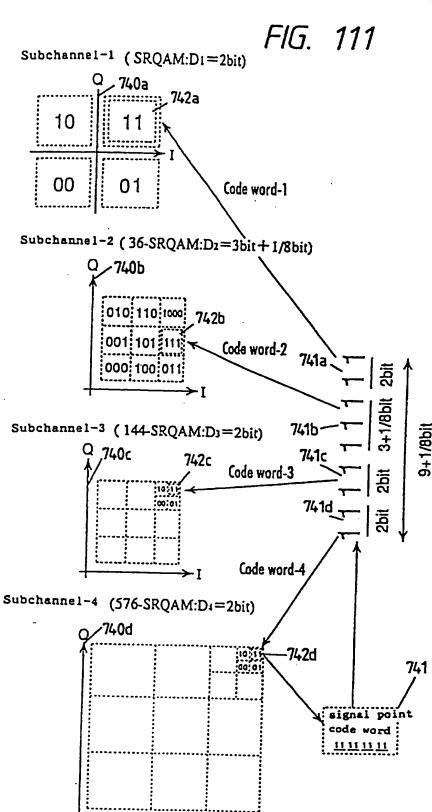




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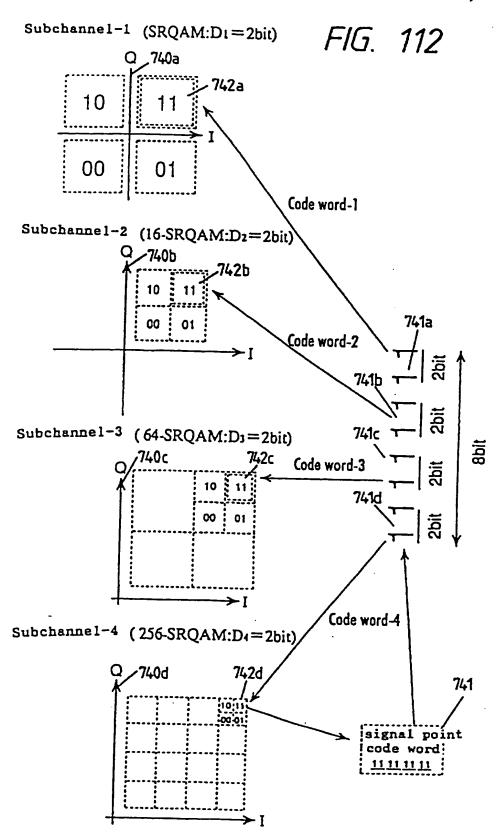
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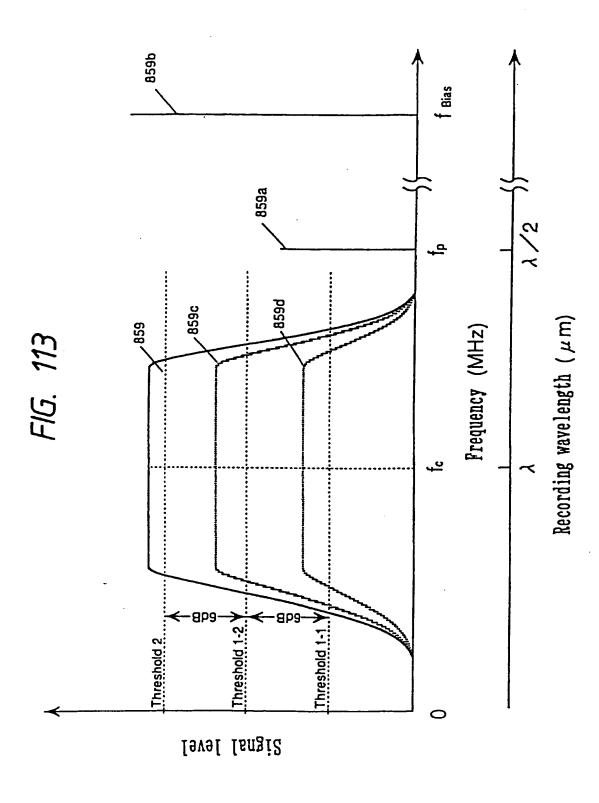
Sep. 1, 1998

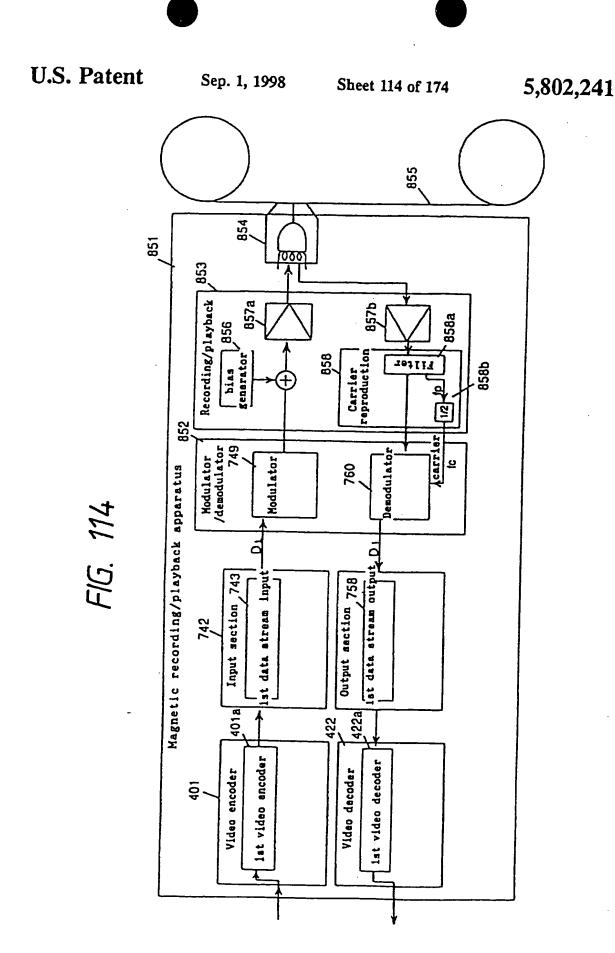
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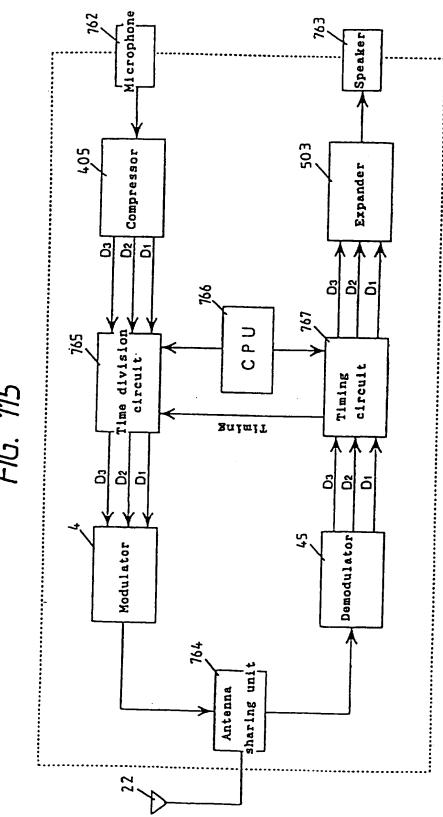


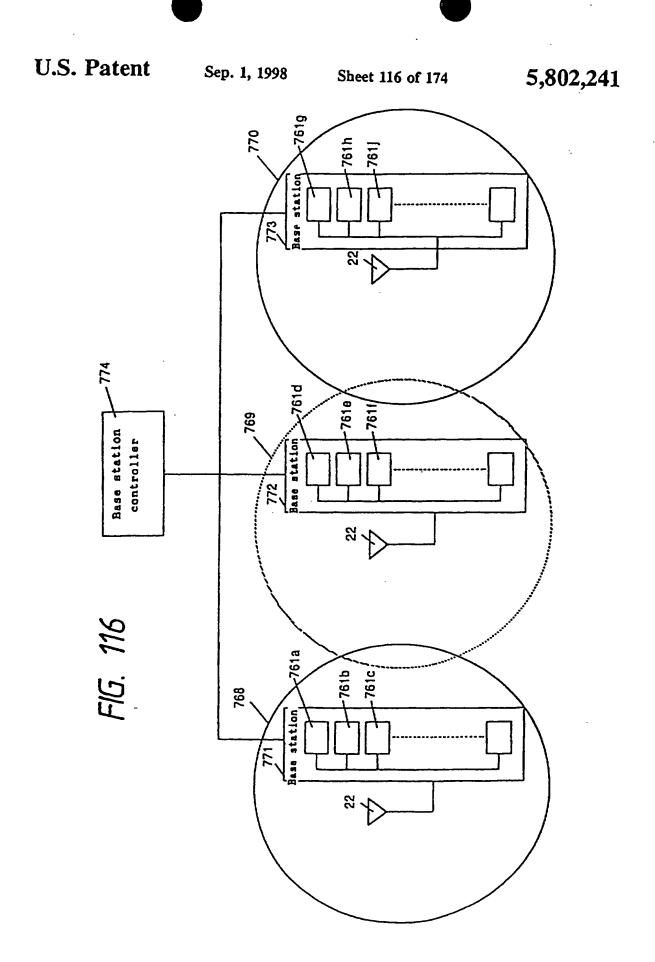
U.S. Patent

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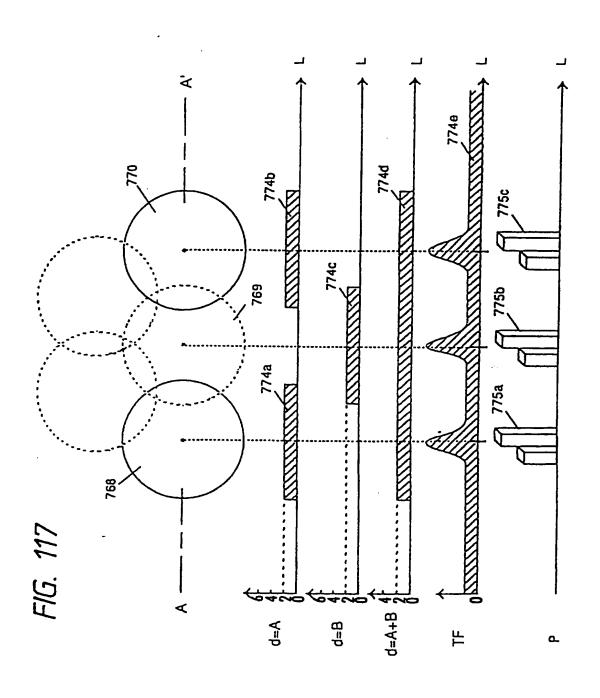
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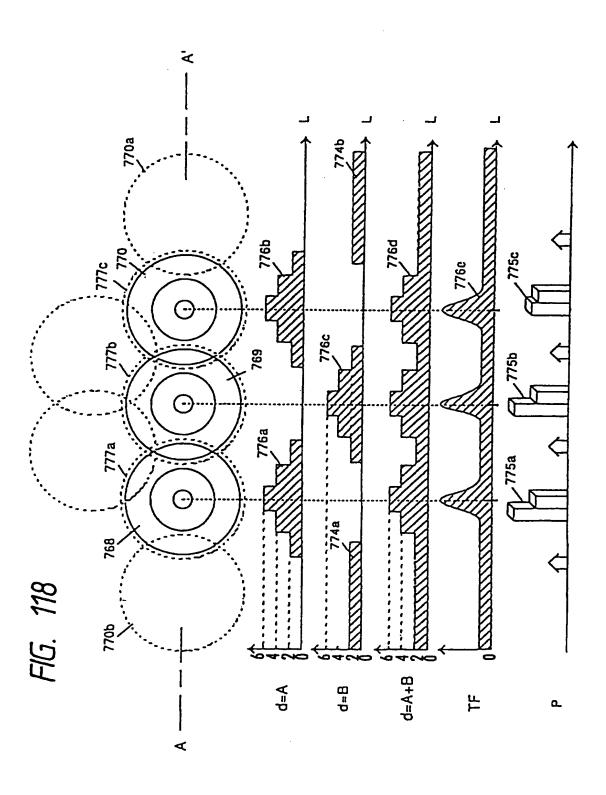


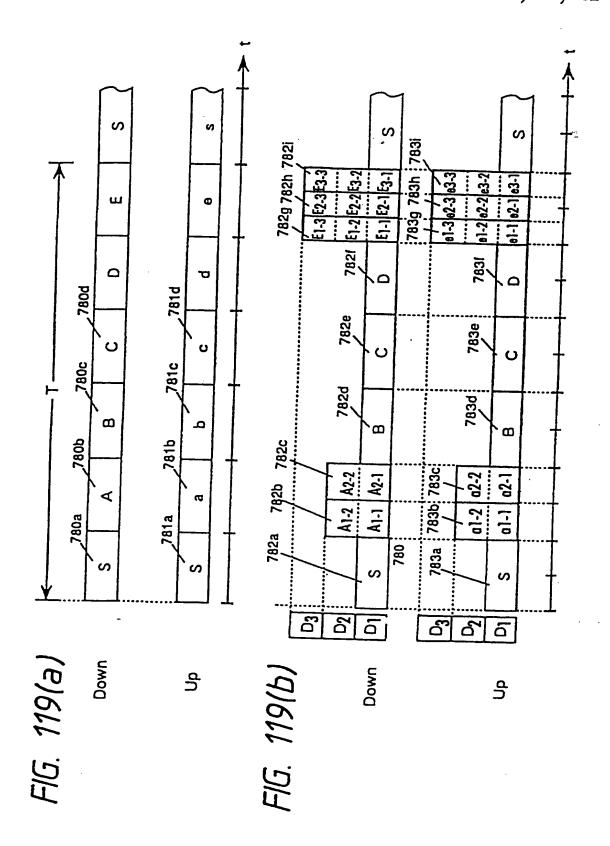










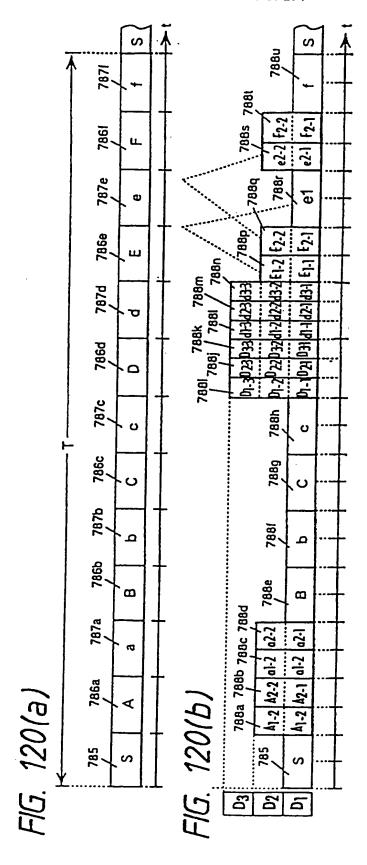


U.S. Patent

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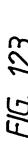
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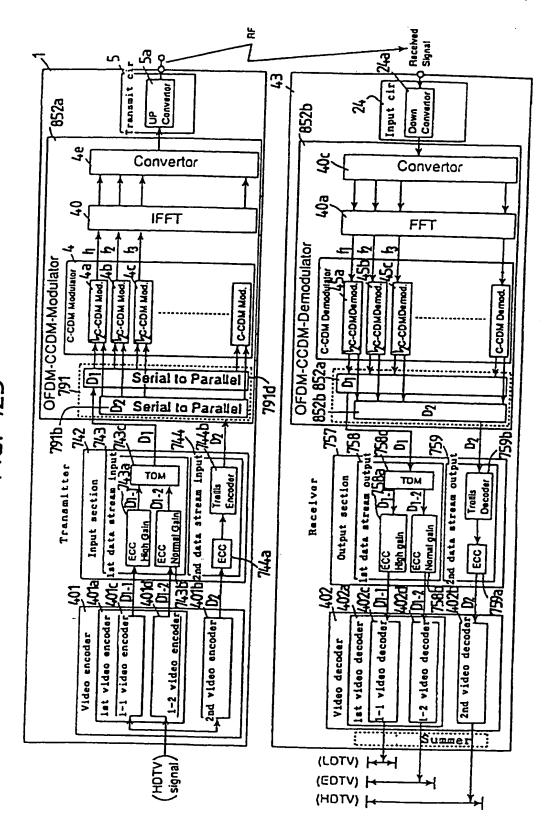
5,802,241



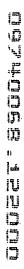
U.S. Patent Sep. 1, 1998 Sheet 121 of 174 5,802,241 Microphone Speaker Compressor Transmitter/receiver Expander õ ō CPU Time division circuit Timing SalmiT ā ō Demodulator Modulator sharing unit Antenna

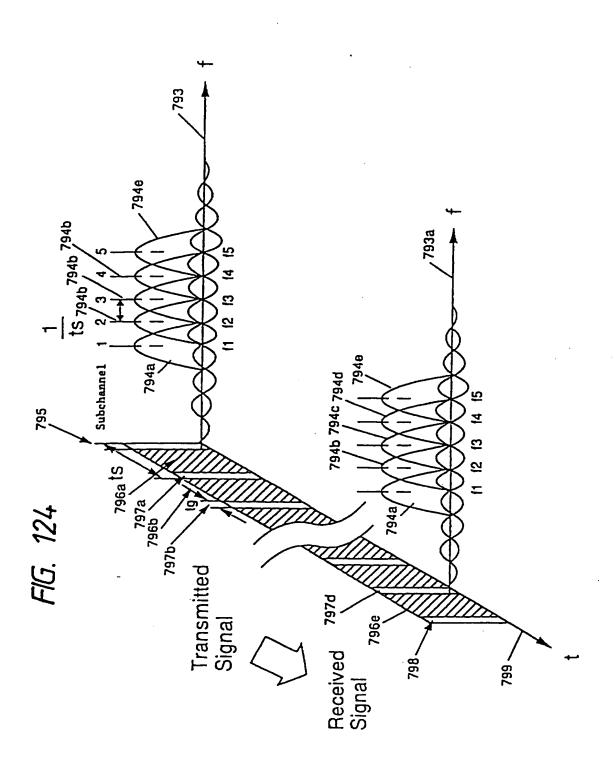
U.S. Patent Sep. 1, 1998 Sheet 122 of 174 5,802,241 Microphone Speaker Compressor Expander Transmitter/receiver õ ŏ ō 992 765 <u>~</u> rime division Timing circuit RaimiT 2 õ ō Demodulator Modulator sharing unit Antenna





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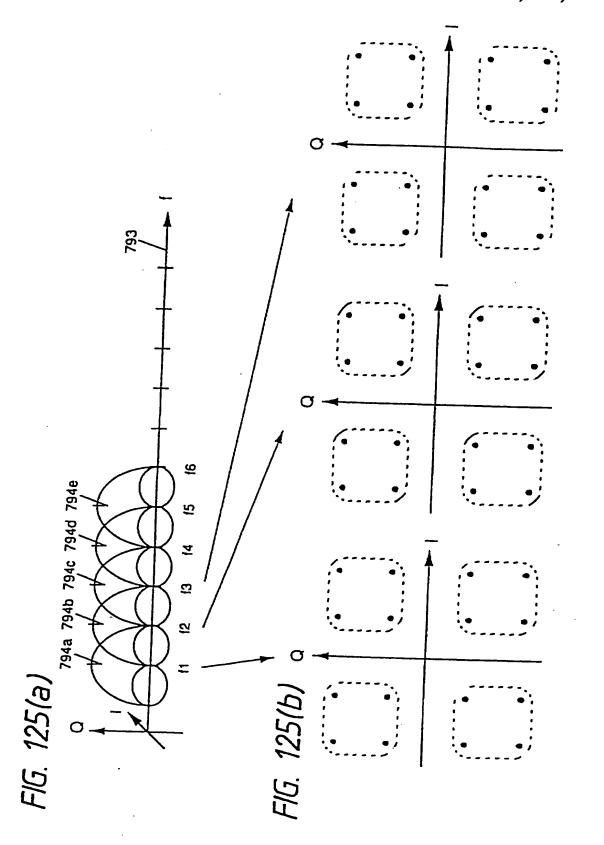




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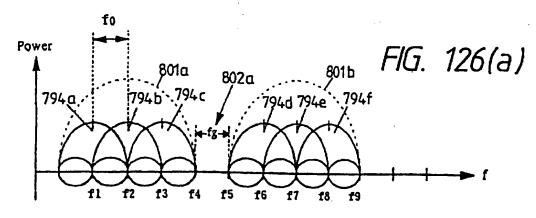
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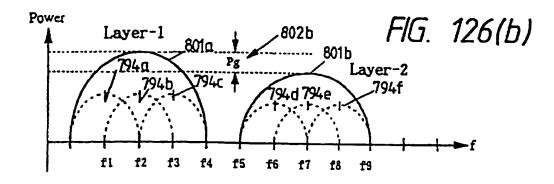
5,802,241

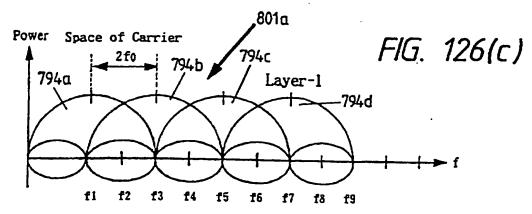


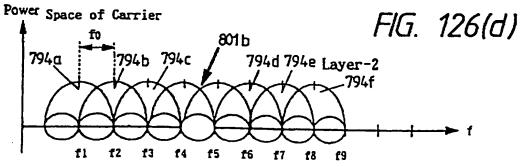
Sep. 1, 1998

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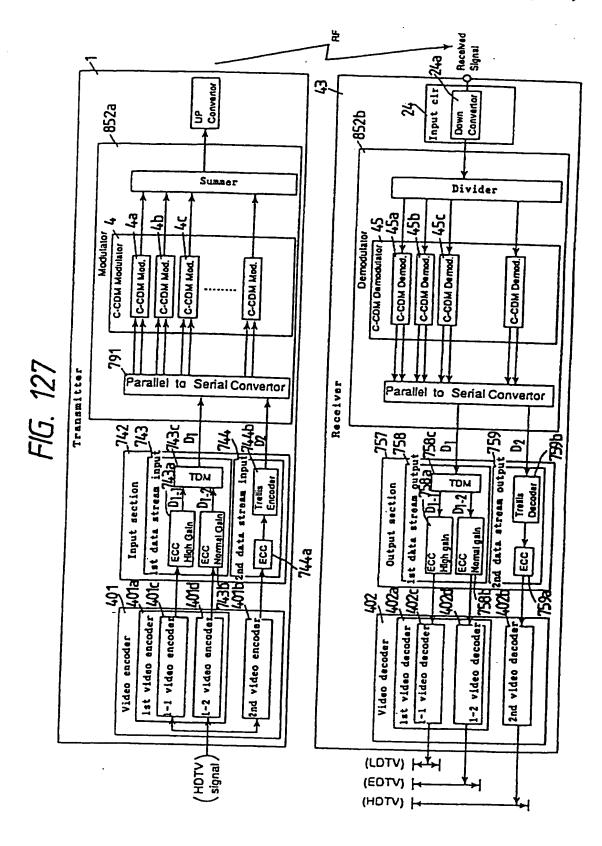




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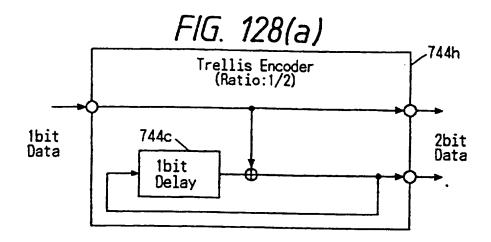


FIG. 128(b)

Trellis Encoder (Ratio:2/3)

2bit Data

744c

744c

744d

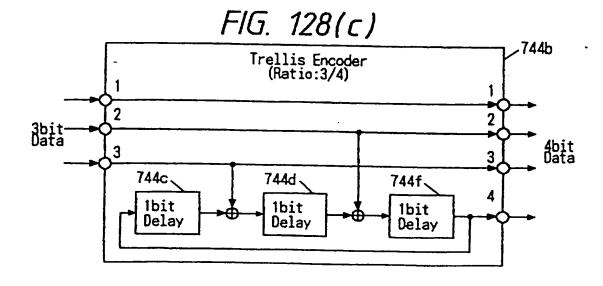
3bit Data

744c

744d

3bit Delay

1bit Delay

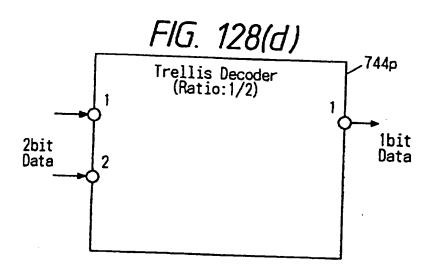


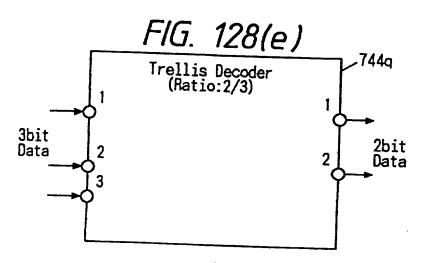


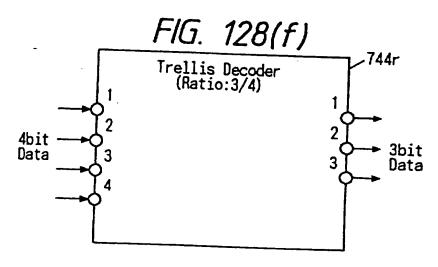
Sep. 1, 1998

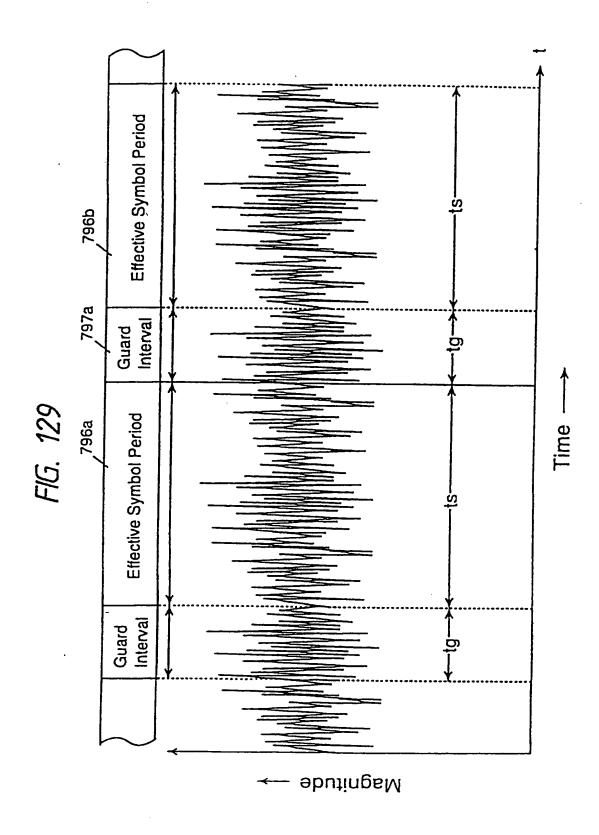
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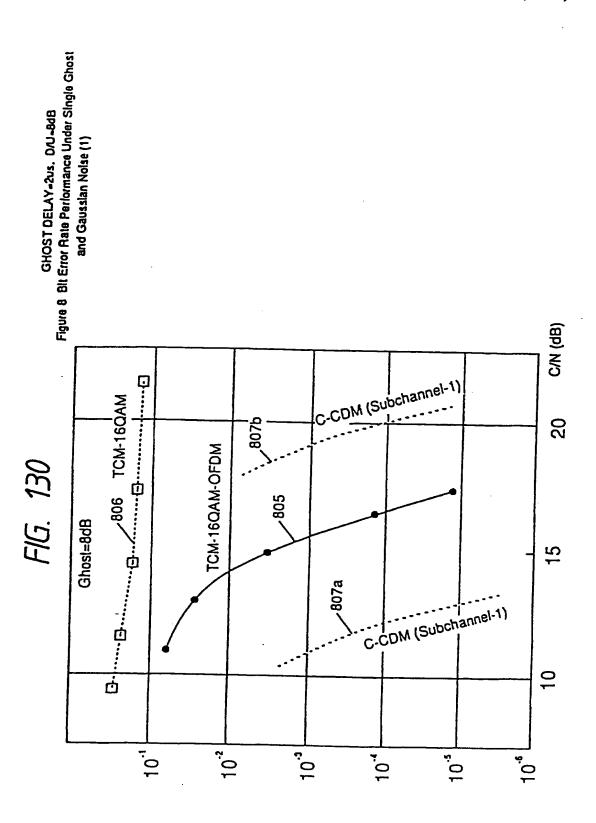


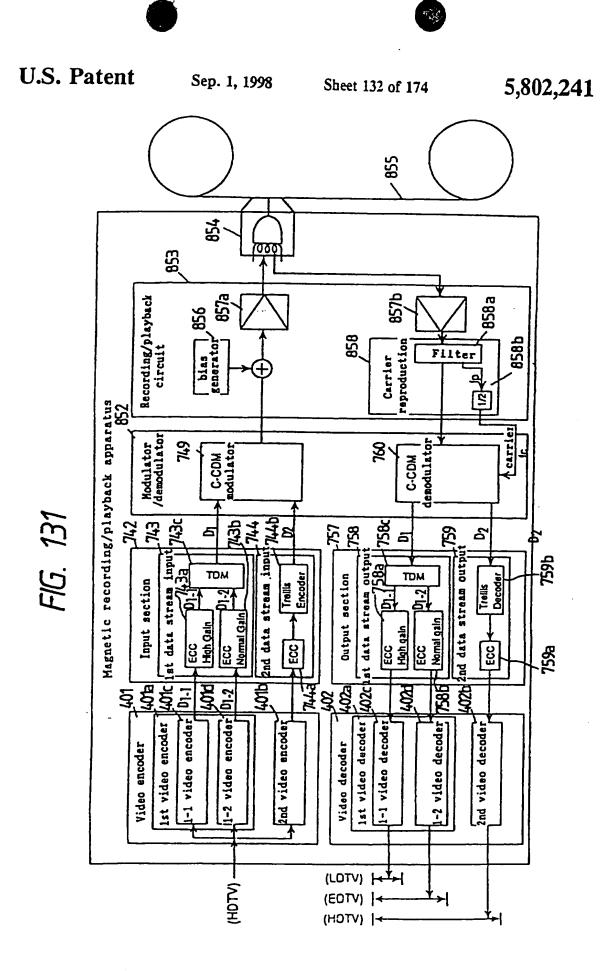


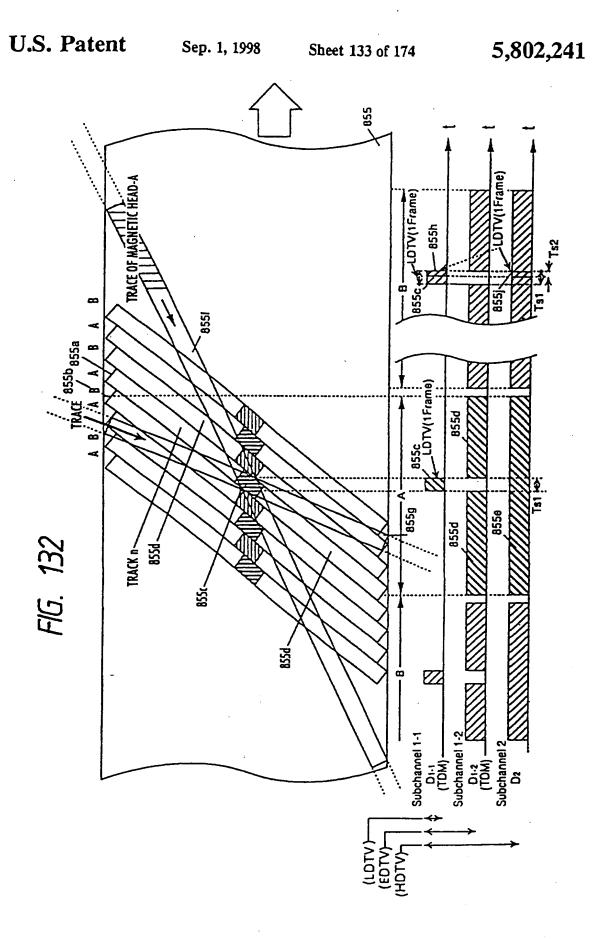






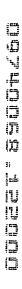


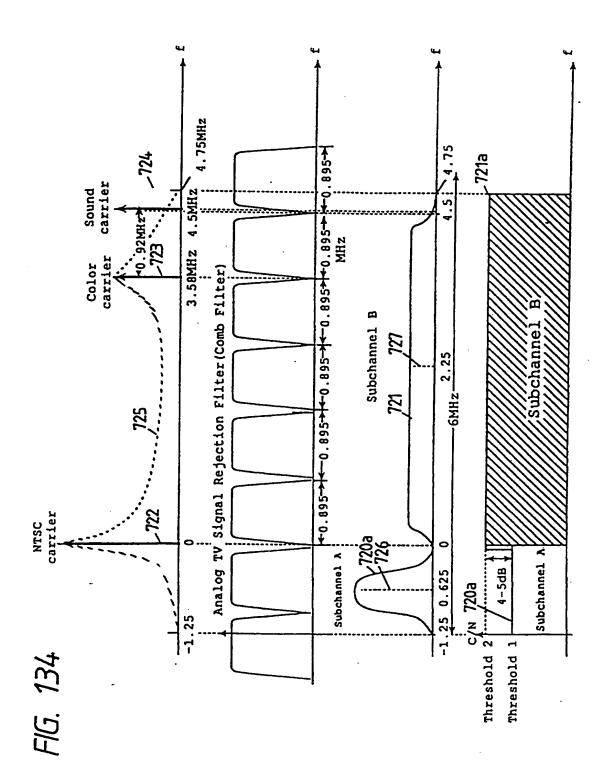




U.S. Patent Sep. 1, 1998 Sheet 134 of 174 5,802,241 **Transmitted** Received Signal Signal sir Sa Converter nput cir Converter Down 3 852b C-CDM demodulator C-CDM modulator Demodulator Hodulator -743c 743 85 gg 742 402b and data stream output at data stream outpu and data stream inpu Transmitter Mat MOT Output section FIG. 133 Receiver Input section st data stream High galn ည္သ PLOF 4gP £01c 7436 P204 7586 £024 75 1st video decoder 1-2 video encoder 2nd video encoder 1-1 video encoder 1st video encoder 1-2 video decoder 1-1 video decodes 2nd video decoder Video encoder Video decoder

(LDTV) --(VTOH) --(HDTV)--



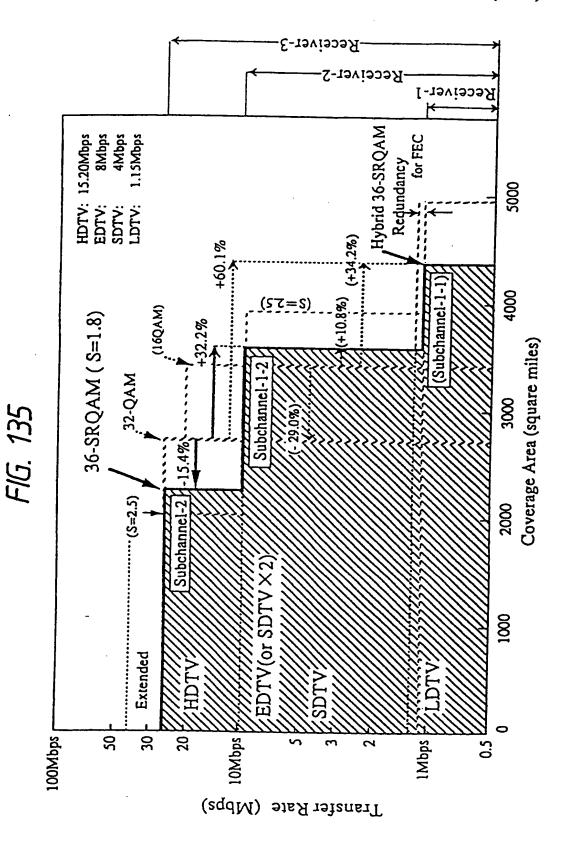




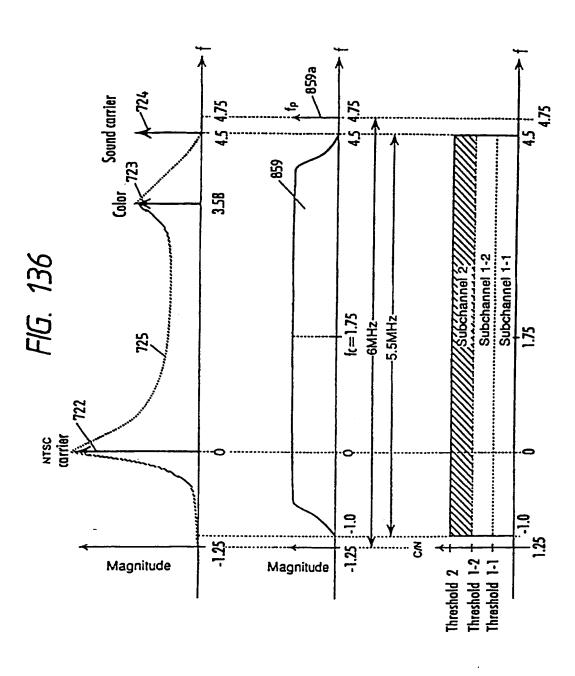


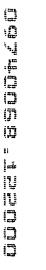
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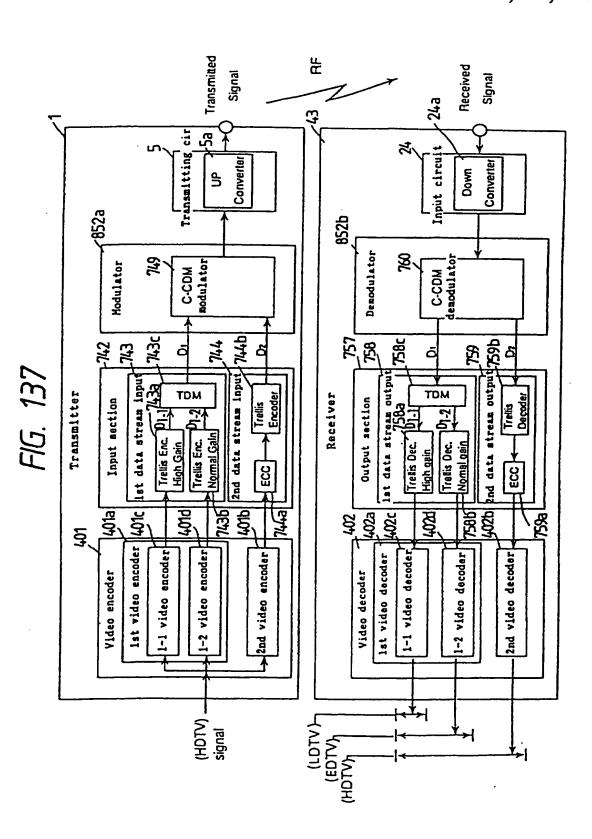
5,802,241











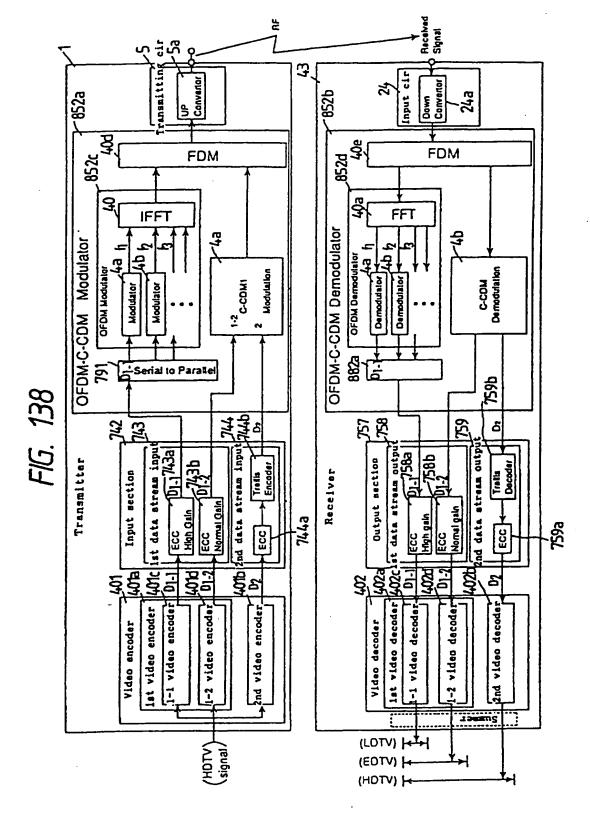
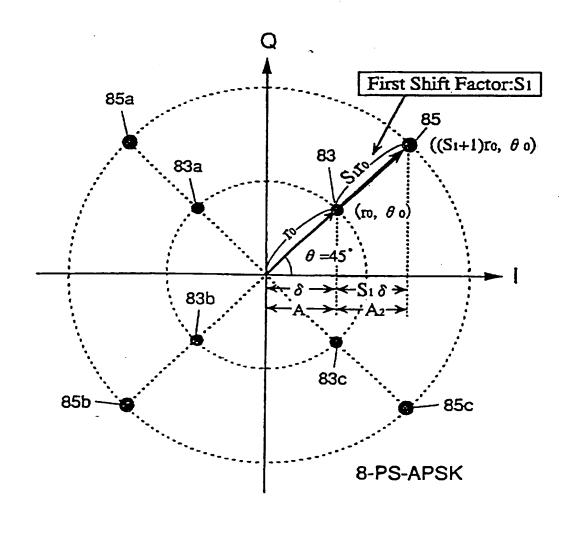


FIG. 139



COVECTE TENDE

FIG. 140

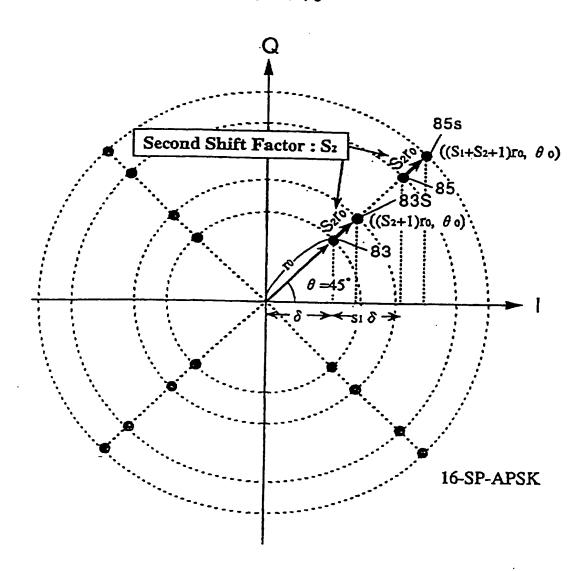


FIG. 141

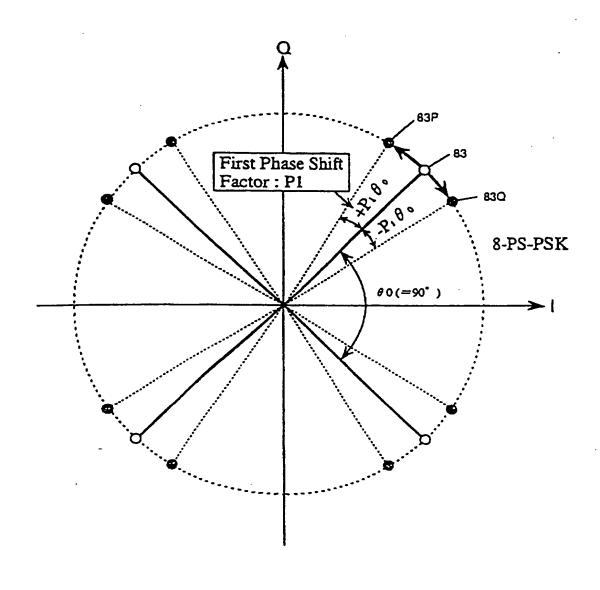
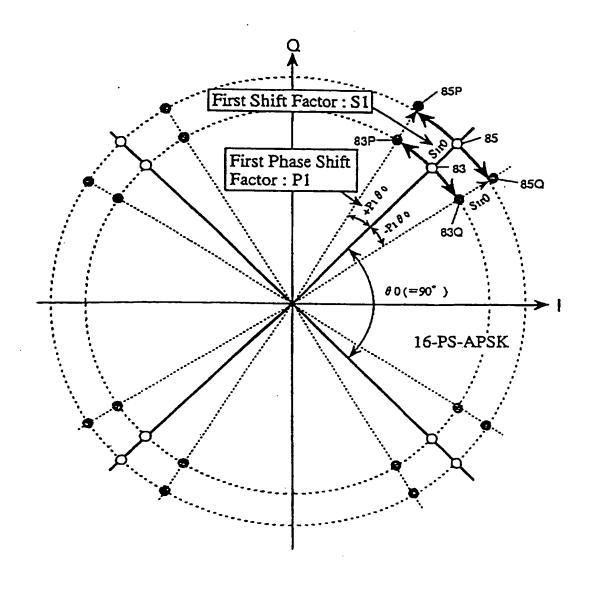
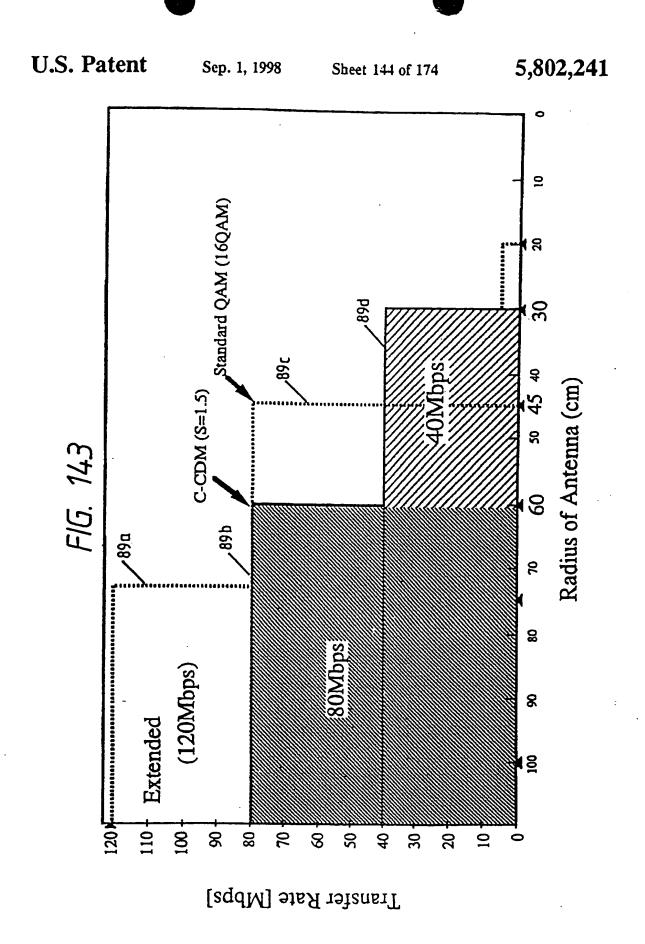
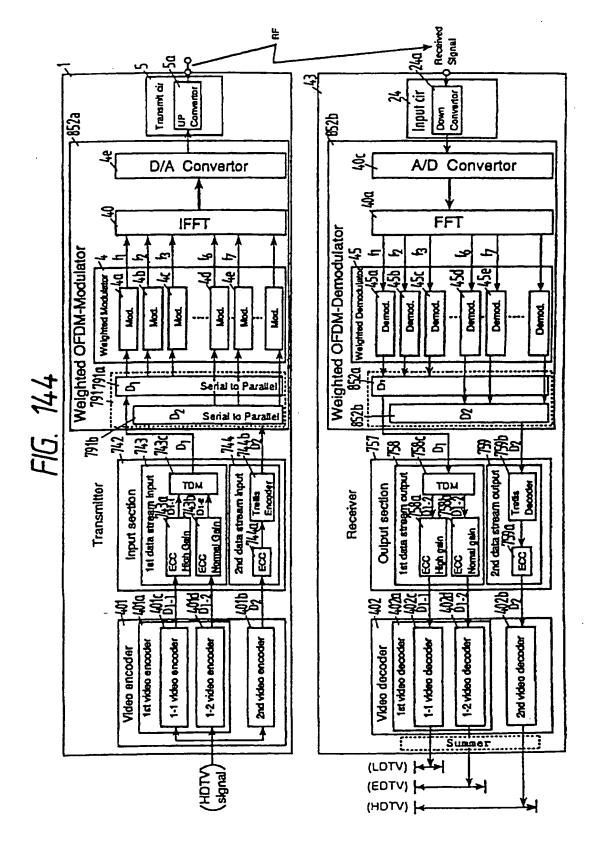


FIG. 142



COSTONE ANDORO





COPHOCE LESONO

DOPLICE LEDDO

FIG. 145(a)

797a
796a
796b

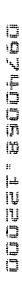
15a
15b
15b
15b
15b
15b
15c
16b
16b
1796b
1796b
1810a
1810a
1810a
1810a
1810a
1810a
1810a
1810a

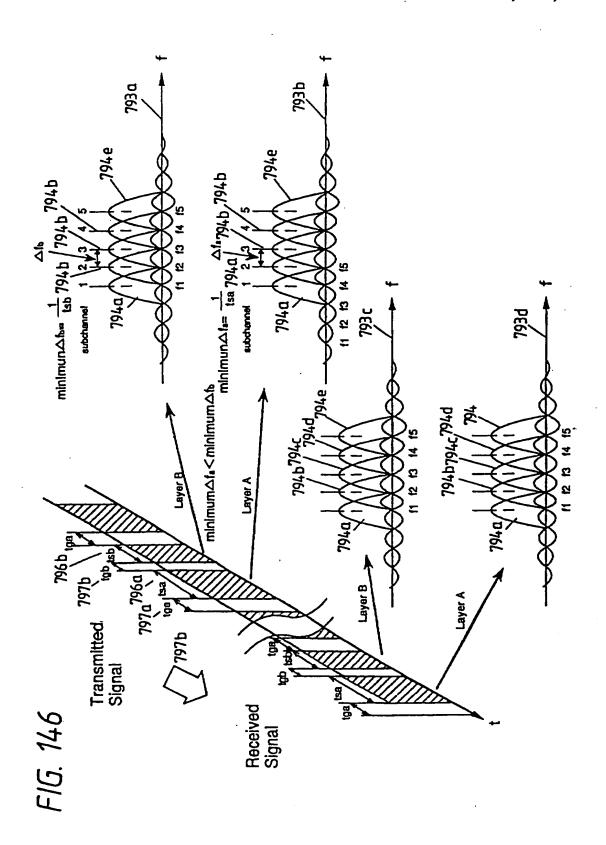
FIG. 145(b)

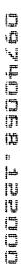
797a
796a
796b
796b

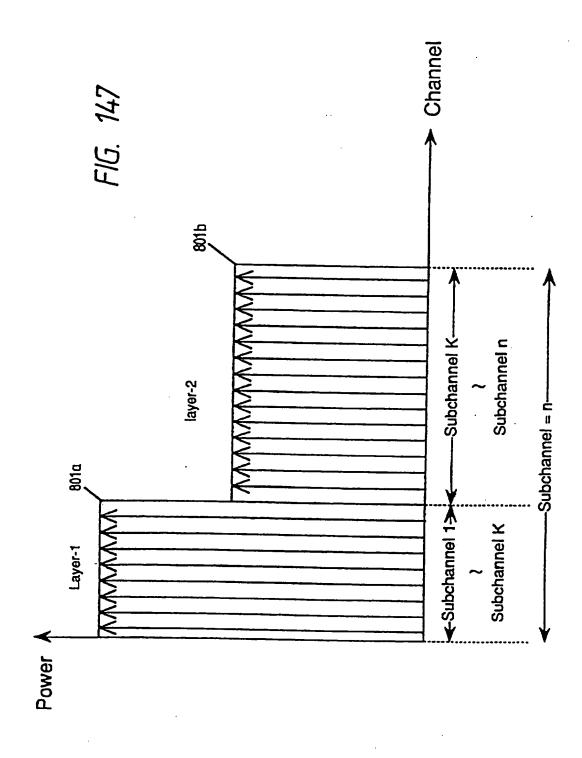
tsa
tsa
B

811a
811b
811c
811d

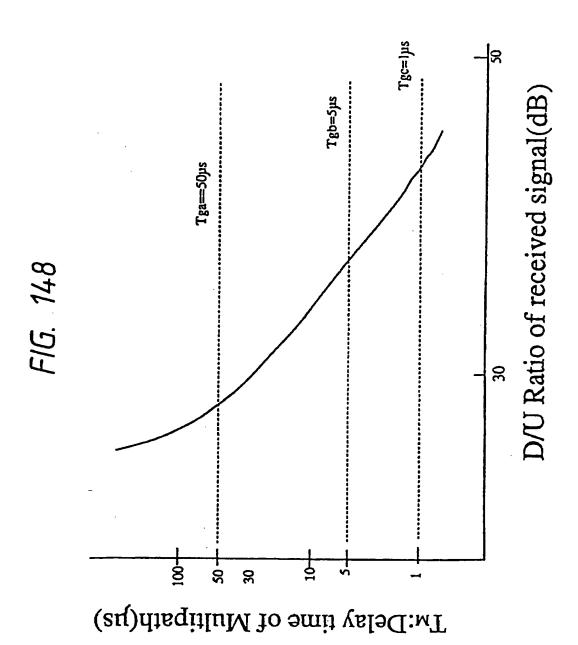












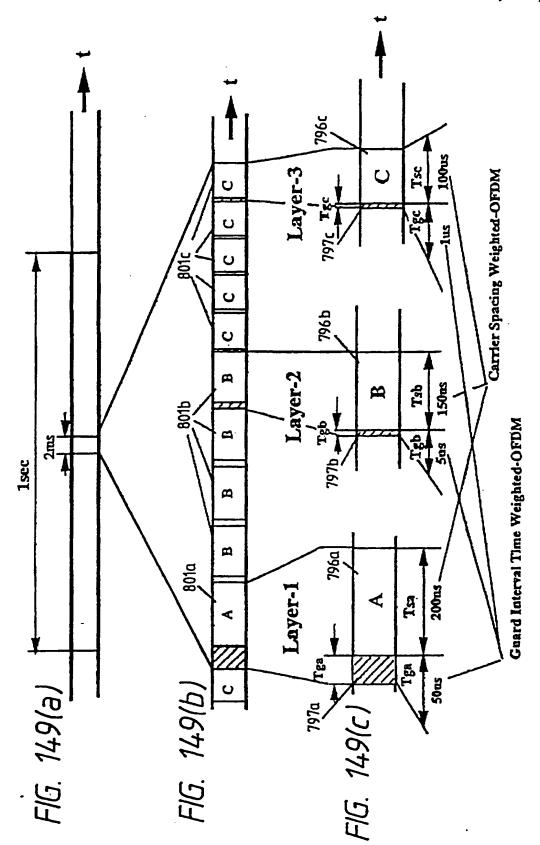


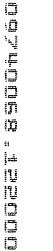
U.S. Patent

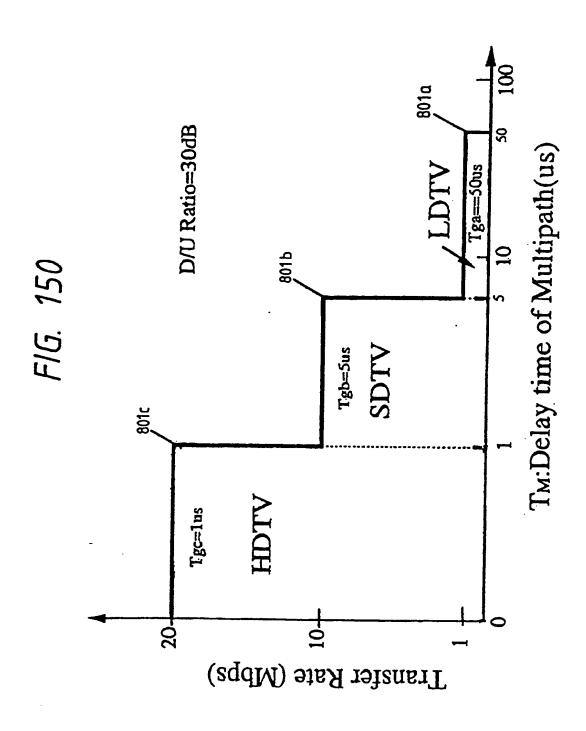
Sep. 1, 1998

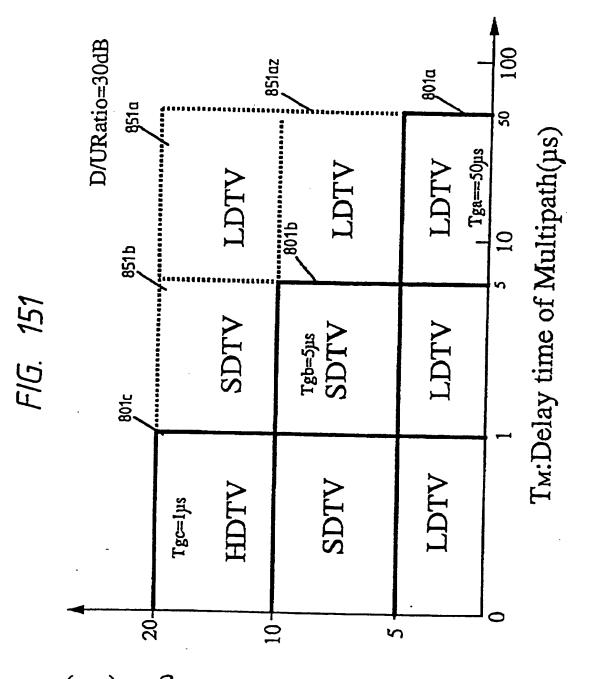
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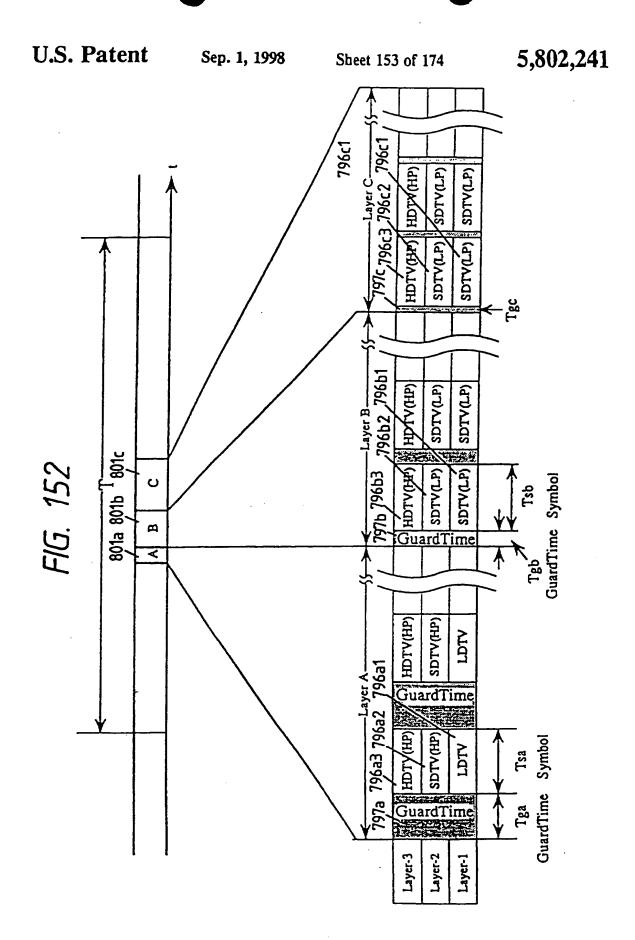


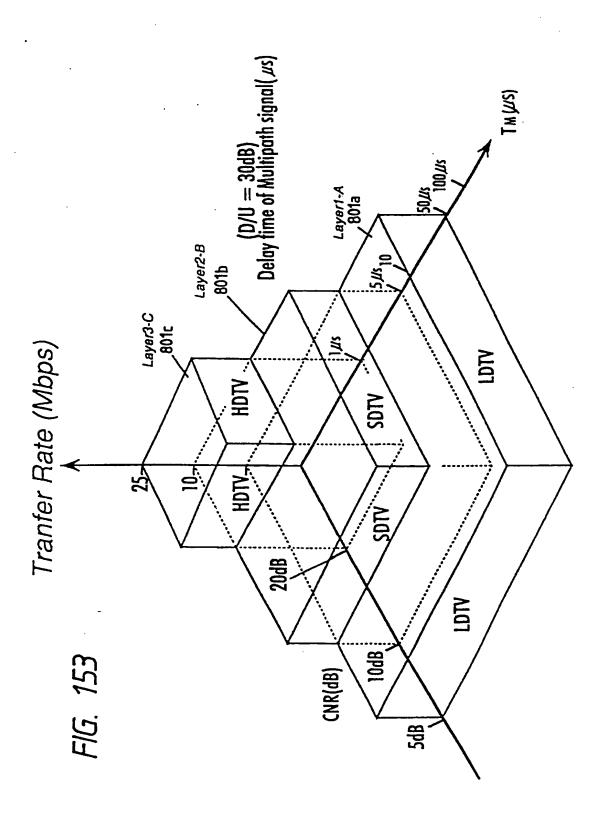




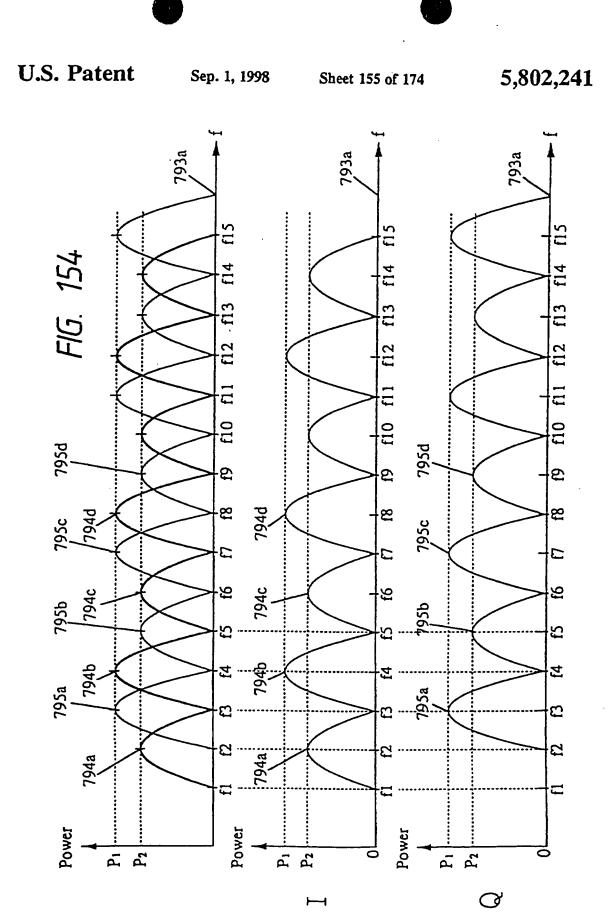


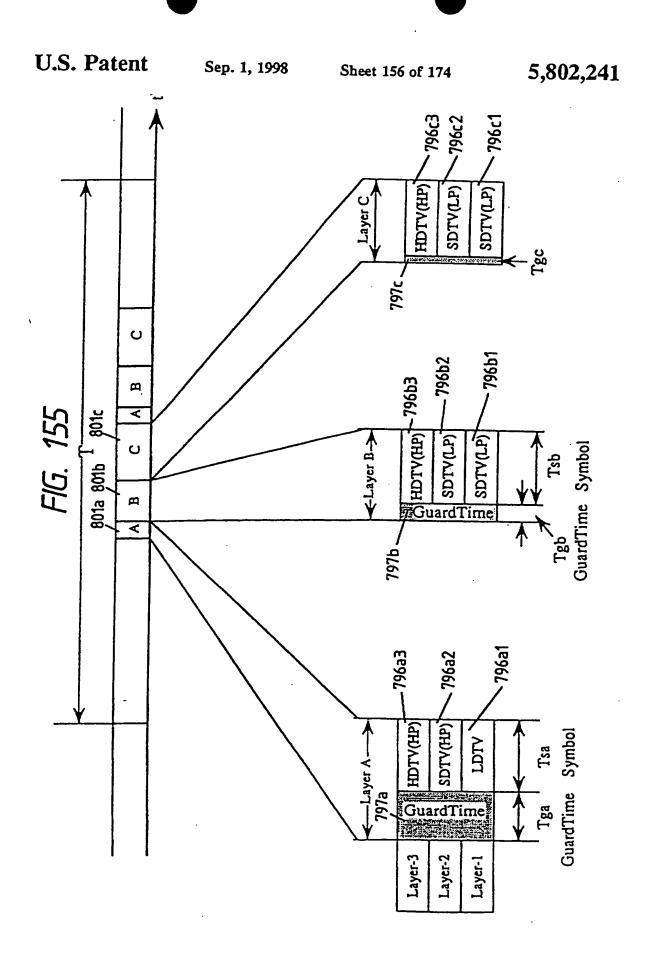
C/N Ratio of Received signal (dB)

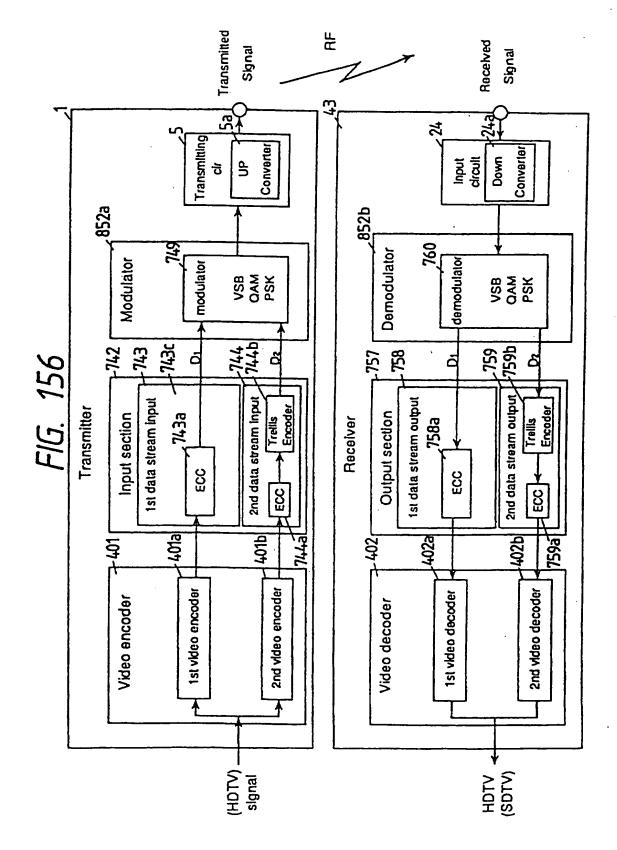




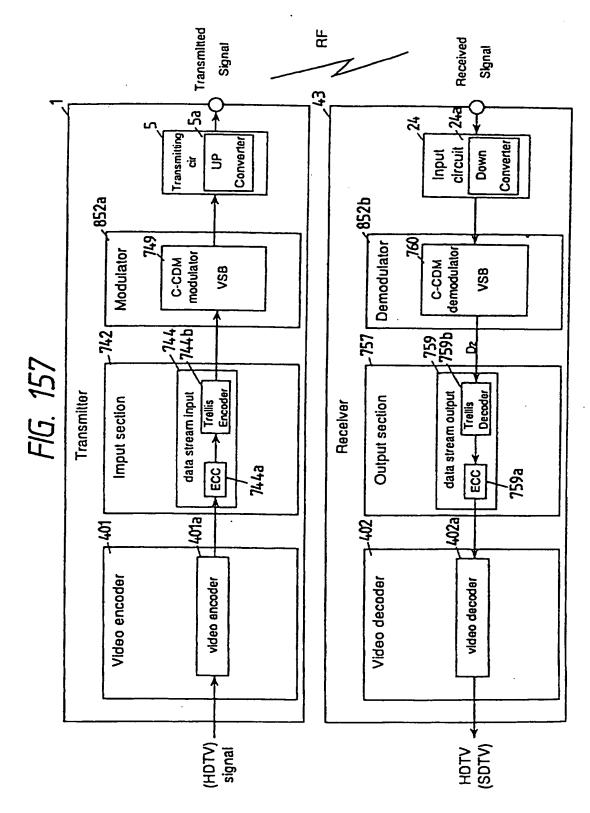
COPHINE LEEDE





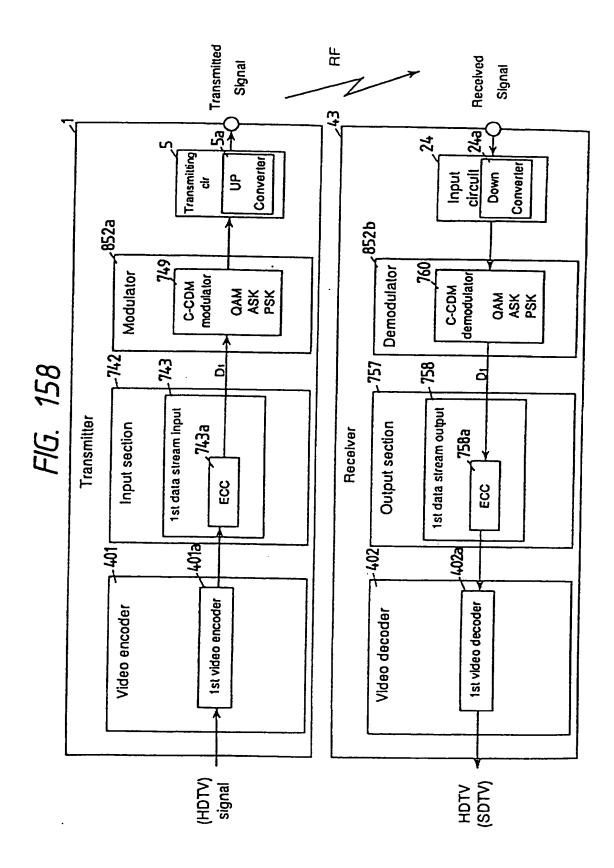


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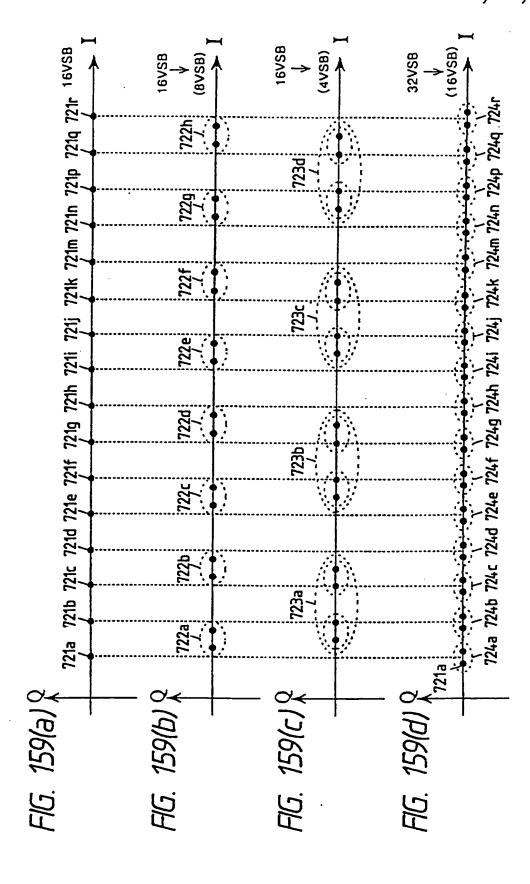


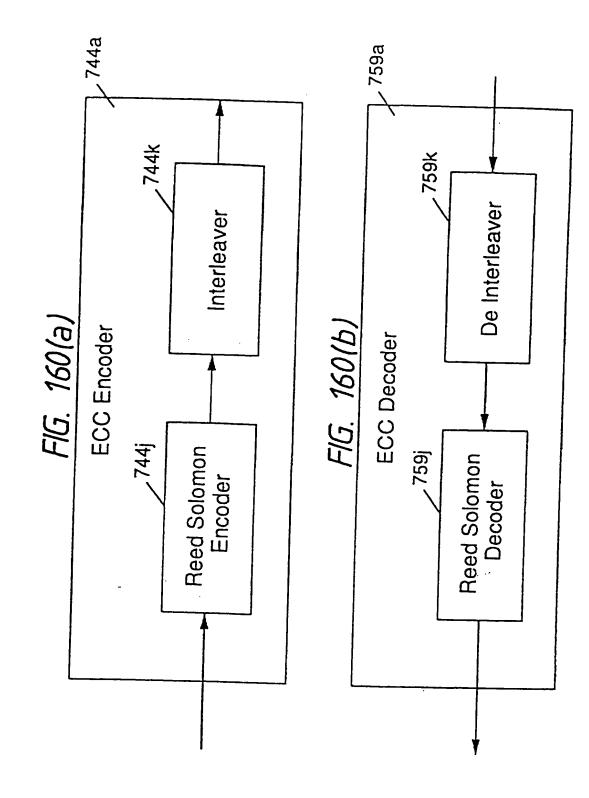




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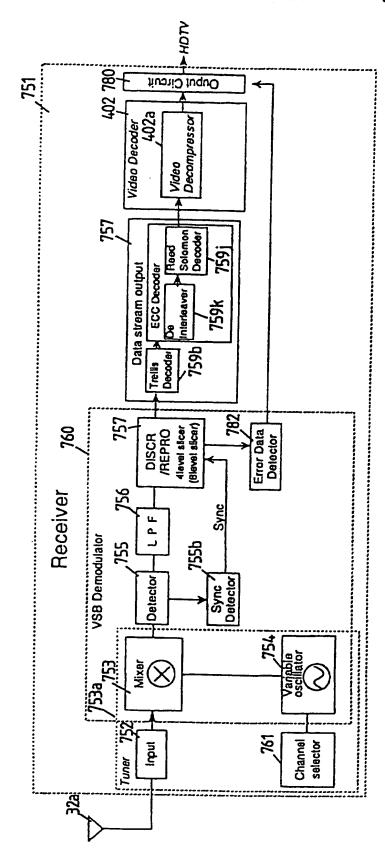
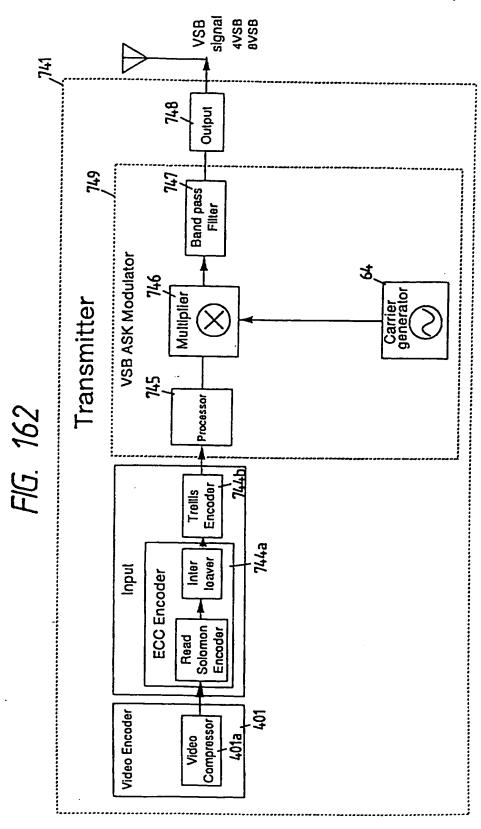
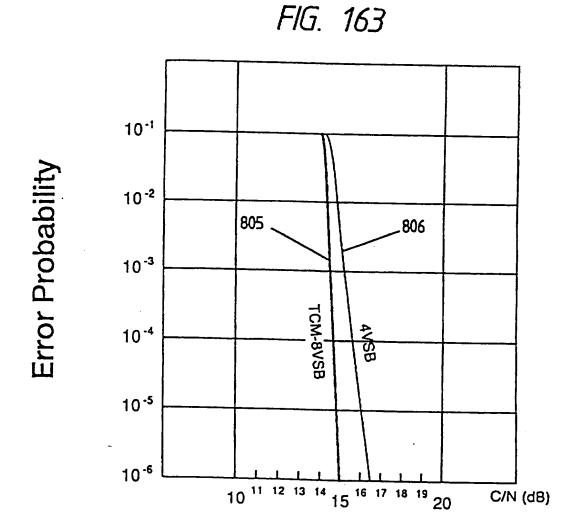


FIG. 16;

Sep. 1, 1998

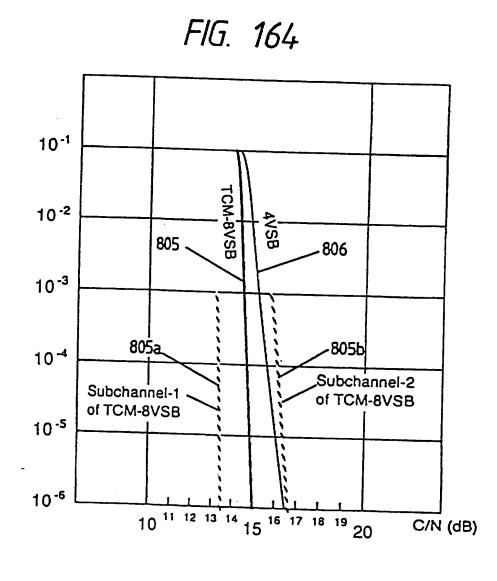
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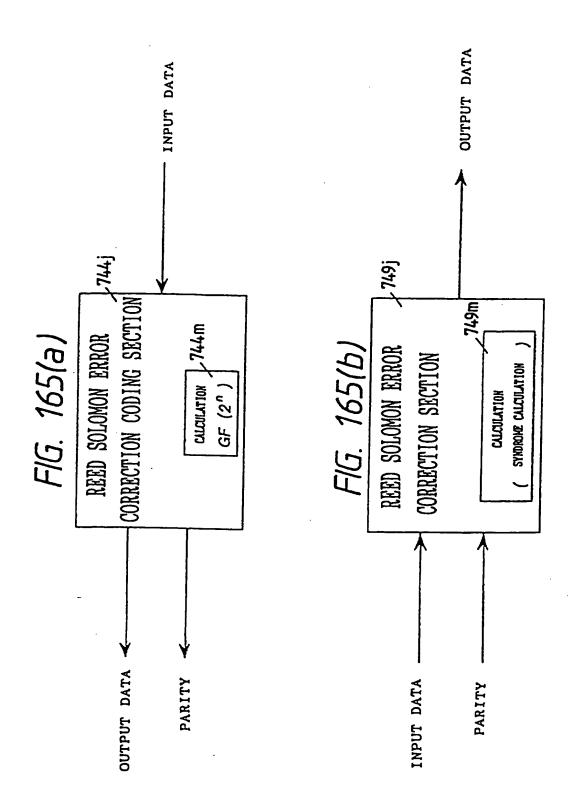




**Error Probability** 

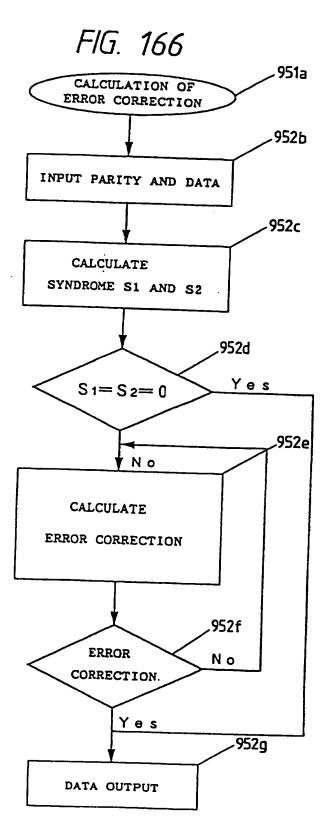
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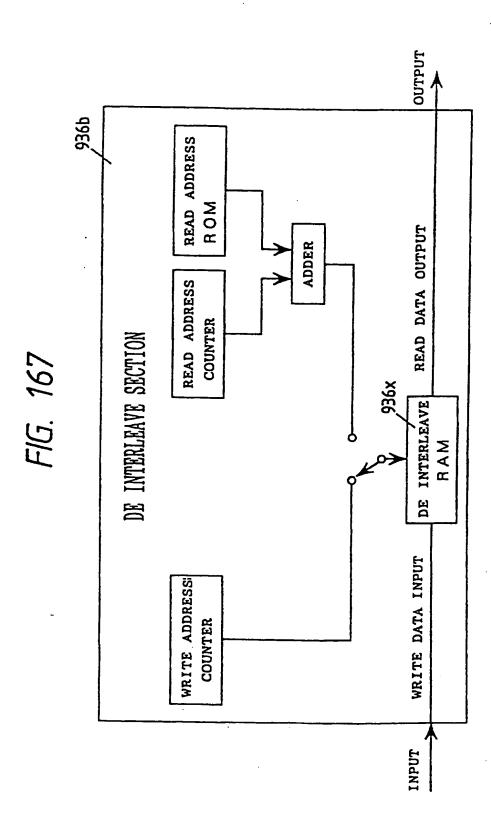




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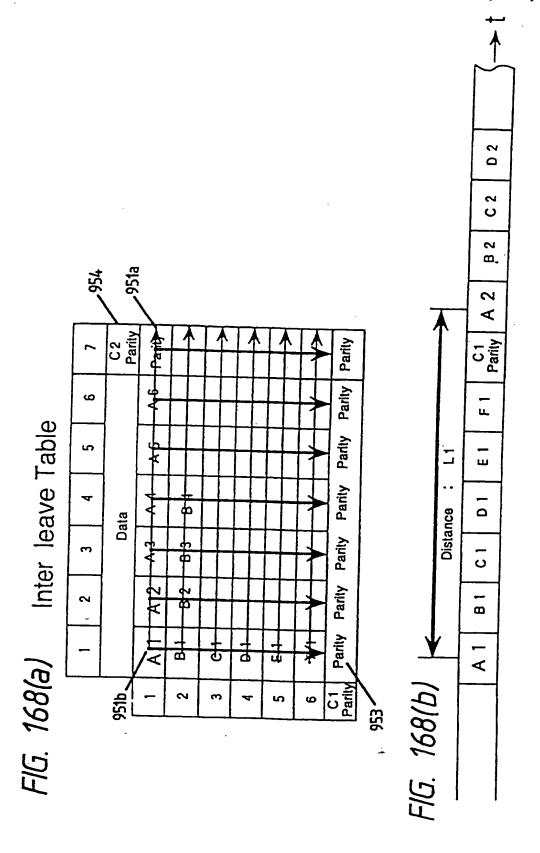
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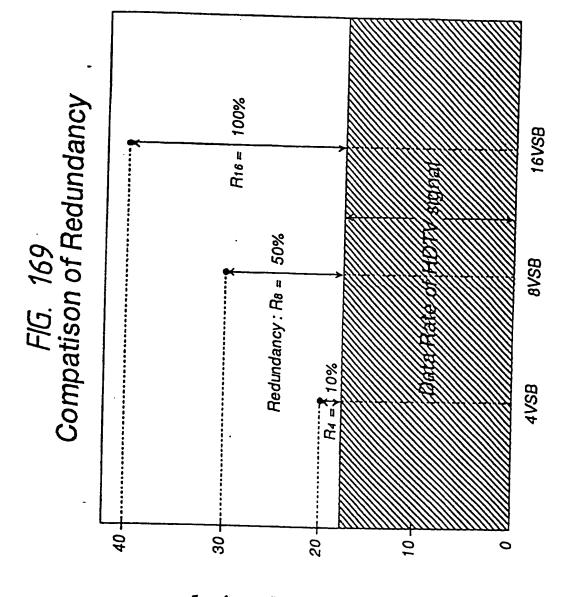




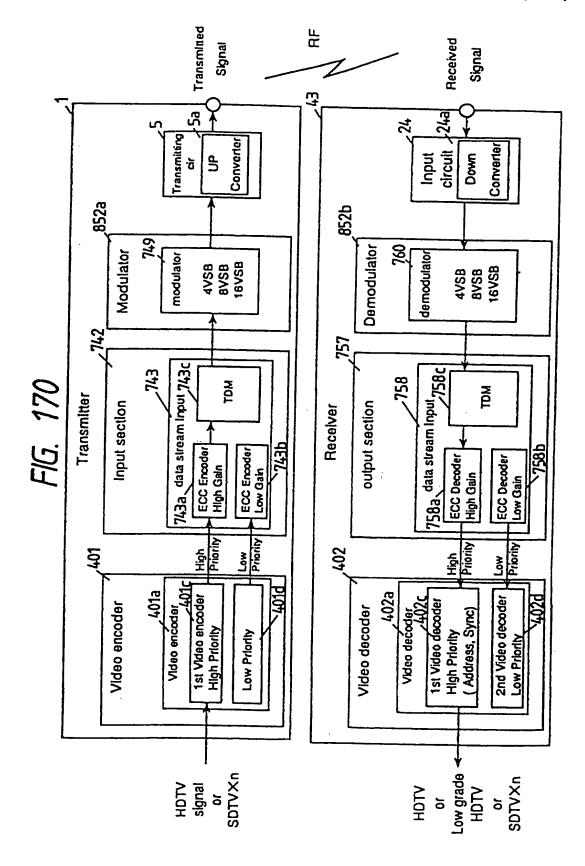
Sep. 1, 1998

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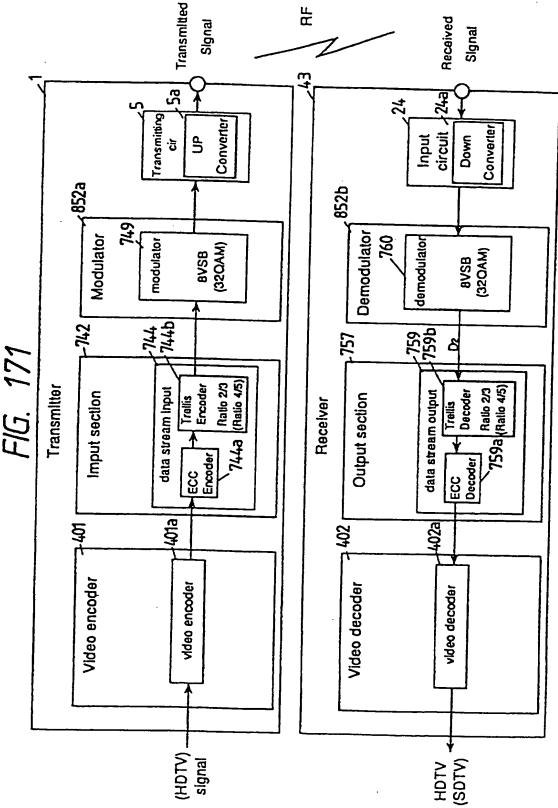




Transfer rate [ Mbps ]



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